

The Student's Guide
to VHDL
Second Edition

The Student's Guide to VHDL

Second Edition

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To my son Alexander

Contents

	Preface	xiii
1	Fundamental Concepts	1
	1.1 Modeling Digital Systems	1
	1.2 Domains and Levels of Modeling	3
	1.2.1 Modeling Example	3
	1.3 Modeling Languages	7
	1.4 VHDL Modeling Concepts	7
	1.4.1 Elements of Behavior	8
	1.4.2 Elements of Structure	10
	1.4.3 Mixed Structural and Behavioral Models	12
	1.4.4 Test Benches	13
	1.4.5 Analysis, Elaboration and Execution	14
	1.5 Learning a New Language: Lexical Elements and Syntax	16
	1.5.1 Lexical Elements	17
	<i>Comments</i>	17
	<i>Identifiers</i>	19
	<i>Reserved Words</i>	20
	<i>Special Symbols</i>	22
	<i>Numbers</i>	22
	<i>Characters</i>	23
	<i>Strings</i>	23
	<i>Bit Strings</i>	24
	1.5.2 Syntax Descriptions	26
	Exercises	29
2	Scalar Data Types and Operations	31
	2.1 Constants and Variables	31
	2.1.1 Constant and Variable Declarations	31
	2.1.2 Variable Assignment	33
	2.2 Scalar Types	34
	2.2.1 Type Declarations	34
	2.2.2 Integer Types	35
	2.2.3 Floating-Point Types	38
	2.2.4 Physical Types	39
	<i>Time</i>	42
	2.2.5 Enumeration Types	43
	<i>Characters</i>	44
	<i>Booleans</i>	46

	<i>Bits</i>	47	
	<i>Standard Logic</i>	48	
	<i>Condition Conversion</i>	49	
2.3	Type Classification	50	
2.3.1	Subtypes	51	
2.3.2	Type Qualification	53	
2.3.3	Type Conversion	53	
2.4	Attributes of Scalar Types	54	
2.5	Expressions and Predefined Operations	57	
	Exercises	61	
3	Sequential Statements		65
3.1	If Statements	65	
3.2	Case Statements	68	
3.3	Null Statements	74	
3.4	Loop Statements	75	
3.4.1	Exit Statements	76	
3.4.2	Next Statements	79	
3.4.3	While Loops	80	
3.4.4	For Loops	82	
3.4.5	Summary of Loop Statements	85	
3.5	Assertion and Report Statements	85	
	Exercises	92	
4	Composite Data Types and Operations		95
4.1	Arrays	95	
4.1.1	Multidimensional Arrays	98	
4.1.2	Array Aggregates	99	
4.1.3	Array Attributes	103	
4.2	Unconstrained Array Types	105	
4.2.1	Predefined Array Types	106	
	<i>Strings</i>	106	
	<i>Boolean Vectors, Integer Vectors, Real Vectors, and Time Vectors</i>	106	
	<i>Bit Vectors</i>	107	
	<i>Standard-Logic Arrays</i>	107	
	<i>String and Bit-String Literals</i>	108	
4.2.2	Unconstrained Array Element Types	109	
4.2.3	Unconstrained Array Ports	111	
4.3	Array Operations and Referencing	114	
4.3.1	Logical Operators	114	
4.3.2	Shift Operators	116	
4.3.3	Relational Operators	117	
	<i>Maximum and Minimum Operations</i>	118	
4.3.4	The Concatenation Operator	119	
4.3.5	To_String Operations	119	
4.3.6	Array Slices	120	
4.3.7	Array Type Conversions	121	

4.3.8	Arrays in Case Statements	124
4.3.9	Matching Case Statements	125
4.4	Records	127
4.4.1	Record Aggregates	130
4.4.2	Unconstrained Record Element Types	130
	Exercises	133

5 Basic Modeling Constructs 135

5.1	Entity Declarations and Architecture Bodies	135
5.1.1	Concurrent Statements	139
5.1.2	Signal Declarations	139
5.2	Behavioral Descriptions	141
5.2.1	Signal Assignment	141
	<i>Conditional Signal Assignments</i>	144
	<i>Selected Signal Assignments</i>	145
5.2.2	Signal Attributes	147
5.2.3	Wait Statements	149
5.2.4	Delta Delays	153
5.2.5	Transport and Inertial Delay Mechanisms	156
5.2.6	Process Statements	162
5.2.7	Concurrent Signal Assignment Statements	164
	<i>Concurrent Simple Signal Assignments</i>	164
	<i>Concurrent Conditional Signal Assignment</i>	165
	<i>Concurrent Selected Signal Assignments</i>	169
5.2.8	Concurrent Assertion Statements	171
5.2.9	Entities and Passive Processes	172
5.3	Structural Descriptions	174
5.4	Design Processing	184
5.4.1	Analysis	184
5.4.2	Design Libraries and Contexts	186
5.4.3	Elaboration	188
5.4.4	Execution	191
	Exercises	192

6 Subprograms 201

6.1	Procedures	201
6.1.1	Return Statement in a Procedure	206
6.2	Procedure Parameters	207
6.2.1	Signal Parameters	211
6.2.2	Default Values	214
6.2.3	Unconstrained Array Parameters	215
6.2.4	Summary of Procedure Parameters	218
6.3	Concurrent Procedure Call Statements	219
6.4	Functions	221
6.4.1	Functional Modeling	224
6.4.2	Pure and Impure Functions	224
6.4.3	The Function now	226

6.5	Overloading	227
6.5.1	Overloading Operator Symbols	228
6.6	Visibility of Declarations	230
	Exercises	234
7	Packages and Use Clauses	239
7.1	Package Declarations	239
7.1.1	Subprograms in Package Declarations	244
7.1.2	Constants in Package Declarations	244
7.2	Package Bodies	246
7.2.1	Local Packages	249
7.3	Use Clauses	251
7.3.1	Visibility of Used Declarations	255
	Exercises	258
8	Resolved Signals	261
8.1	Basic Resolved Signals	261
8.1.1	Composite Resolved Subtypes	266
8.1.2	Summary of Resolved Subtypes	271
8.1.3	IEEE std_logic_1164 Resolved Subtypes	272
8.2	Resolved Signals, Ports, and Parameters	274
8.2.1	Resolved Ports	276
8.2.2	Driving Value Attribute	279
8.2.3	Resolved Signal Parameters	280
	Exercises	281
9	Predefined and Standard Packages	287
9.1	The Predefined Packages standard and env	287
9.2	IEEE Standard Packages	290
9.2.1	Standard VHDL Mathematical Packages	290
	<i>Real Number Mathematical Package</i>	290
	<i>Complex Number Mathematical Package</i>	293
9.2.2	The std_logic_1164 Multivalued Logic System	295
9.2.3	Standard Integer Numeric Packages	298
9.2.4	Package Summary	307
	<i>Operator Overloading Summary</i>	307
	<i>Conversion Function Summary</i>	309
	<i>Strength Reduction Function Summary</i>	311
	Exercises	312
10	Aliases	315
10.1	Aliases for Data Objects	315
10.2	Aliases for Non-Data Items	320
	Exercises	323

11	Generic Constants	325
	11.1 Generic Constants	325
	Exercises	332
12	Components and Configurations	335
	12.1 Components	335
	12.1.1 Component Declarations	335
	12.1.2 Component Instantiation	337
	12.1.3 Packaging Components	338
	12.2 Configuring Component Instances	340
	12.2.1 Basic Configuration Declarations	340
	12.2.2 Configuring Multiple Levels of Hierarchy	343
	12.2.3 Direct Instantiation of Configured Entities	346
	12.2.4 Generic and Port Maps in Configurations	347
	12.2.5 Deferred Component Binding	353
	Exercises	355
13	Generate Statements	359
	13.1 Generating Iterative Structures	359
	13.2 Conditionally Generating Structures	365
	Exercises	372
14	Design for Synthesis	375
	14.1 Synthesizable Subsets	375
	14.2 Use of Data Types	376
	14.2.1 Scalar Types	377
	14.2.2 Composite and Other Types	378
	14.3 Interpretation of Standard Logic Values	379
	14.4 Modeling Combinational Logic	380
	14.5 Modeling Sequential Logic	383
	14.5.1 Modeling Edge-Triggered Logic	384
	14.5.2 Level-Sensitive Logic and Inferring Storage	392
	14.5.3 Modeling State Machines	394
	14.6 Modeling Memories	396
	14.7 Synthesis Attributes	400
	14.8 Metacomments	410
	Exercises	411
15	Case Study: System Design Using the Gumnut Core	413
	15.1 Overview of the Gumnut	413
	15.1.1 Instruction Set Architecture	413
	15.1.2 External Interface	418
	<i>The Gumnut Entity Declaration</i>	420
	<i>Instruction and Data Memories</i>	421
	15.2 A Digital Alarm Clock	425
	15.2.1 System Design	425

15.2.2	Synthesizing and Implementing the Alarm Clock	433
	Exercises	435

A	Standard Packages	437
A.1	The Predefined Package standard	437
A.2	The Predefined Package env	441
A.3	The Predefined Package textio	441
A.4	Standard VHDL Mathematical Packages	443
A.4.1	The math_real Package	443
A.4.2	The math_complex Package	445
A.5	The std_logic_1164 Multivalued Logic System Package	446
A.6	Standard Integer Numeric Packages	450
A.6.1	The numeric_bit Package	450
A.6.2	The numeric_std Package	456
A.6.3	The numeric_bit_unsigned Package	457
A.6.4	The numeric_std_unsigned Package	459
B	VHDL Syntax	461
B.1	Design File	463
B.2	Library Unit Declarations	463
B.3	Declarations and Specifications	465
B.4	Type Definitions	468
B.5	Concurrent Statements	470
B.6	Sequential Statements	472
B.7	Interfaces and Associations	475
B.8	Expressions and Names	476
C	Answers to Exercises	479
	References	497
	Index	499

Preface

VHDL is a language for describing digital electronic systems. It arose out of the United States government's Very High Speed Integrated Circuits (VHSIC) program. In the course of this program, it became clear that there was a need for a standard language for describing the structure and function of integrated circuits (ICs). Hence the VHSIC Hardware Description Language (VHDL) was developed. It was subsequently developed further under the auspices of the Institute of Electrical and Electronic Engineers (IEEE) and adopted in the form of the IEEE Standard 1076, Standard VHDL Language Reference Manual, in 1987. This first standard version of the language is often referred to as VHDL-87.

Like all IEEE standards, the VHDL standard is subject to review from time to time. Comments and suggestions from users of the 1987 standard were analyzed by the IEEE working group responsible for VHDL, and in 1992 a revised version of the standard was proposed. This was eventually adopted in 1993, giving us VHDL-93. A second round of revision of the standard was started in 1998. That process was completed in 2001, giving us VHDL-2002. After that, further development took place in the IEEE working group and in a technical committee of an organization, Accellera, whose charter is to promote standards for electronics design. These efforts led to the current version of the language, VHDL-2008, described in this book.

VHDL is designed to fill a number of needs in the design process. First, it allows description of the structure of a system, that is, how it is decomposed into subsystems and how those subsystems are interconnected. Second, it allows the specification of the function of a system using familiar programming language forms. Third, as a result, it allows the design of a system to be simulated before being manufactured, so that designers can quickly compare alternatives and test for correctness without the delay and expense of hardware prototyping. Fourth, it allows the detailed structure of a design to be synthesized from a more abstract specification, allowing designers to concentrate on more strategic design decisions and reducing time to market.

This book presents a structured guide to the modeling facilities offered by the VHDL language, showing how they can be used for the design of digital systems. The book does not purport to teach digital design, since that topic is large enough by itself to warrant several textbooks covering its various aspects. Instead, the book assumes that the reader has at least a basic grasp of digital design concepts, such as might be gained from a first course in digital design in an engineering degree program. Some exposure to computer programming and to concepts of computer organization will also be beneficial. *The Student's Guide to VHDL* is a condensed edition of *The Designer's Guide to VHDL*, which is a complete reference describing all of the features in the language. In *The Student's Guide*, we have selected those features that are most commonly used in the educational context, making the book suitable for use in an introductory or intermediate-level course in digital or computer design. Those who need a complete language reference for use in advanced courses or professional practice may prefer to use *The Designer's Guide*.

One pervasive theme running through the presentation in this book is that modeling a system using a hardware description language is essentially a software design exercise. This implies that good software engineering practice should be applied. Hence the treatment in this book draws directly from experience in software engineering. There are numerous hints and techniques from small-scale and large-scale software engineering presented throughout the book, with the sincere intention that they might be of use to readers.

Structure of the Book

The Student's Guide to VHDL is organized so that it can be read linearly from front to back. This path offers a graduated development, with each chapter building on ideas introduced in the preceding chapters. Each chapter introduces a number of related concepts or language facilities and illustrates each one with examples.

Chapter 1 introduces the idea of a hardware description language and outlines the reasons for its use and the benefits that ensue. It then proceeds to introduce the basic concepts underlying VHDL, so that they can serve as a basis for examples in subsequent chapters. The next three chapters cover the aspects of VHDL that are most like conventional programming languages. These may be used to describe the behavior of a system in algorithmic terms. Chapter 2 explains the basic type system of the language and introduces the scalar data types. Chapter 3 describes the sequential control structures, and Chapter 4 covers composite data structures used to represent collections of data elements. In Chapter 5, the main facilities of VHDL used for modeling hardware are covered in detail. These include facilities for modeling the basic behavioral elements in a design, the signals that interconnect them and the hierarchical structure of the design.

The next group of chapters extends this basic set of facilities with language features that make modeling of large systems more tractable. Chapter 6 introduces procedures and functions, which can be used to encapsulate behavioral aspects of a design. Chapter 7 introduces the package as a means of collecting together related parts of a design or of creating modules that can be reused in a number of designs. Chapter 8 deals with the important topic of resolved signals, and Chapter 9 describes a number of predefined and standard packages for use in VHDL designs.

The third group of chapters covers advanced modeling features in VHDL. Chapter 10 covers aliases as a way of managing the large number of names that arise in a large model. Chapter 11 describes generic constants as a means of parameterizing the behavior and structure of a design. Chapter 12 deals with the topics of component instantiation and configuration. These features are important in large real-world models, but they can be difficult to understand. Hence this book introduces structural modeling through the mechanism of direct instantiation in earlier chapters and leaves the more general case of component instantiation and configuration until this later chapter. In Chapter 13, generated regular structures are covered.

In the last two chapters, we focus on synthesis. Chapter 14 covers guidelines for writing synthesizable models. Then Chapter 15 is a case study, showing development of a synthesizable processor core and its use in a small embedded system, a digital alarm clock.

Each chapter in the book is followed by a set of exercises designed to help the reader develop understanding of the material. Where an exercise relates to a particular topic de-

scribed in the chapter, the section number is included in square brackets. An approximate “difficulty” rating is also provided, expressed using the following symbols:

- ❶ quiz-style exercise, testing basic understanding
- ❷ basic modeling exercise—10 minutes to half an hour effort
- ❸ advanced modeling exercise—one half to two hours effort
- ❹ modeling project—half a day or more effort

Answers for the first category of exercises are provided in Appendix C. The remaining categories involve developing VHDL models. Readers are encouraged to test correctness of their models by running them on a VHDL simulator. This is a much more effective learning exercise than comparing paper models with paper solutions.

Changes in the Second Edition

The first edition of this book was published in 1998, not long after VHDL-93 had gained acceptance. The latest revision of the language, VHDL-2008, adds a number of significant new language features, making this edition of *The Student's Guide to VHDL* significantly bigger than its predecessor. VHDL-2008 also specifies numerous minor new features and changes to existing features to enhance the usability of the language. This edition integrates descriptions of several of the new and revised features into the text. The differences between the various versions are highlighted in call-outs within the text, headed with “VHDL-2002,” “VHDL-93,” or “VHDL-87,” as appropriate. In addition, some of the material has been removed or rearranged. There is a greater emphasis on synthesis in this edition. What was an appendix on the topic in previous editions of *The Designer's Guide to VHDL* has been substantially revised and promoted to full chapter status in this book. Finally, this edition includes a listing of the main VHDL standard packages as an appendix for reference.

Resources for Help and Information

Although this book covers many of the features of VHDL, there will no doubt be questions that it does not answer. For these, the reader will need to seek other resources.

Accellera is one of a number of organizations that sponsors the EDA Industry Working Groups Web server (www.eda.org). The server has links to Web pages and repositories of several VHDL standards groups and user groups.

Readers who have access to the Usenet electronic news network will find the news group `comp.lang.vhdl` a valuable resource. This discussion group is a source of announcements, sample models, questions and answers and useful software. Participants include VHDL users and people actively involved in the language standard working group and in VHDL tool development. The “frequently asked questions” (FAQ) file for this group is a mine of useful pointers to books, products and other information. It is archived at www.eda.org.

This book contains numerous examples of VHDL models that may also serve as a resource for resolving questions. The VHDL source code for these examples and the case

studies, as well as other related information, is available on the companion website for the book at books.elsevier.com/companions/9781558608658.

Although I have been careful to avoid errors in the example code, there are no doubt some that I have missed. I would be pleased to hear about them, so that I can correct them in the on-line code and in future printings of this book. Errata and general comments can be e-mailed to me at vhdl-book@ashenden.com.au.

Acknowledgments

The seeds for this book go back to 1990 when I developed a brief set of notes, *The VHDL Cookbook*, for my computer architecture class at the University of Adelaide. At the time, there were few books on VHDL available, so I made my booklet available for on-line access. News of its availability spread quickly around the world, and within days, my e-mail in-box was bursting. At the time of writing this, nearly 20 years later, I still regularly receive messages about the *Cookbook*. Many of the respondents urged me to write a full textbook version. With that encouragement, I embarked upon the exercise that led to the first edition of *The Designer's Guide to VHDL*. Two years after publication of *The Designer's Guide*, the need for a book specifically for students became evident. That led to publication of the first edition of *The Student's Guide to VHDL*. I am grateful to the many engineers, students and teachers around the world who gave me the impetus to write these books and who made them such a success. I hope this new edition will continue to meet the need for a VHDL reference for student use.

In the previous editions of *The Designer's Guide* and *The Student's Guide*, I had the opportunity to extend thanks to the many people who assisted in development of the books. They included my colleagues at the University of Adelaide; my research collaborators, Phil Wilsey at the University of Cincinnati and Perry Alexander at the University of Kansas; the staff at Morgan Kaufmann Publishers, including, in particular, Denise Penrose; the reviewers of the manuscript for the first edition, namely, Poras Balsara of the University of Texas, Paul Menchini of Menchini & Associates, David Pitts of GTE Labs and the University of Lowell and Philip Wilsey of the University of Cincinnati; David Bishop for his contribution to the material on synthesis in the first edition of *The Designer's Guide*; and Mentor Graphics Corporation, for use of their ModelSim simulator to check the example models. I remain grateful to all of these people and organizations for their valuable contributions to the earlier editions and to this edition.

For the current edition, I would also like to thank Jim Lewis, who collaborated on a recent book, *VHDL-2008: Just the New Stuff*. Much of the material from that book has found its way into this book in one form or another. Thanks also to Mentor Graphics Corporation for continued use of the ModelSim simulator to check the example code. I continue to enjoy an excellent working relationship with the staff at Morgan Kaufmann Publishers and their parent company, Elsevier. Thanks to Chuck Glaser, Senior Acquisitions Editor, for his support in the continued development of these VHDL books; to Dawnmarie Simpson, Senior Project Manager in the Production Department, for her meticulous attention to detail; and to Denise Penrose, Publisher, for her longstanding support of my writing endeavors.

I have dedicated *The Designer's Guide to VHDL* to my wife Katrina, in appreciation of her understanding, encouragement, and support during the writing and revising of that

book. The first edition of *The Student's Guide to VHDL* I dedicated to our son, Alexander, who was very young at that time. Since then, he has maintained his keen interest in learning and intellectual inquiry, as well as a great sense of humor. It is fitting that I dedicate this second edition to my young scholar also.