

CURRICULUM VITAE

David A. Patterson
E.H. and M.E. Pardee Professor of Computer Science
Computer Science Division
Department of Electrical Engineering and Computer Sciences
University of California
Berkeley, California 94720

Education

Ph.D. Computer Science, 1976, UCLA
A.B. Mathematics, 1969

Experience

University of California, Berkeley
Department of Electrical Engineering and Computer Sciences

7/92 to present: E.H. and M.E. Pardee Professor of Computer Science

7/85 to 6/92: Professor

7/81 to 6/85: Associate Professor

1/77 to 6/81: Assistant Professor

10/98 to 10/00: Chief Scientist, Sun Microsystems, Inc.
Network Attached Storage Division

8/89 to 12/89: Scientist, Thinking Machines Corporation, Cambridge, MA
Leave of absence to work on Connection Machine-5

9/79 to 12/79: Principal Engineer, Digital Equipment Corp.
Distributed and Mid-Range Advanced Systems Development, design of higher level microprogramming tools for VAX computers

2/72 to 12/76: Member of the Technical Staff, Hughes Aircraft Company
Data Systems Division, Advanced Projects Laboratory Design of instruction set, CPU architecture, microprogram, microprogram algorithms, microprogramming language and compiler, and microlevel architecture for an aerospace minicomputer. Worked on microcomputer applications, bit slice computers, microprogramming commercial minicomputers, and performance evaluation using a hardware monitor

Honors and Awards

1. UCLA Engineering Award, Outstanding Academic Alumni, 2009
2. MT Connect IMTS, Dinner in honor of David A Patterson's work, 2008
3. AAAS Fellow, 2008
4. Computer History Museum Hall of Fame, 2008
5. ACM-IEEE Eckert Mauchly Award, 2008
6. EECS U.C. Berkeley Technical Achievement Award, 2008
7. ACM Distinguished Service Award, 2007
8. Fellow of the American Association for the Advancement of Science, 2007
9. Fellow of the Computer History Museum, 2007
10. Winner of Inaugural Katayanagi Prize for Research Excellence, 2007
11. Member of the National Academy of Sciences, 2006
12. Fellow of the American Academy of Arts and Sciences, 2006
13. Computing Research Association Distinguished Service Award, 2006
14. Upsilon Pi Epsilon Abacus Award, 2006
15. Member of the Silicon Valley Engineering Hall of Fame, 2005
16. Certificate of Congressional Recognition from Congressman Michael Honda, 2005
17. NEC Foundation for Computers & Communication Promotion ("C&C") Prize, 2004
18. Lifetime National Associate of the National Academies, 2002
19. IEEE James H. Mulligan, Jr., Education Medal, 2000
20. IEEE von Neumann Medal, 2000
21. IEEE Johnson Award for Storage Technology, 1999
22. SIGMOD Test of Time Award, 1998
23. UC Berkeley EECS Diane McIntyre Award for Distinguished Teaching, 1998

24. IEEE Undergraduate Teaching Award, 1996
25. IEEE Computer Society Award for Outstanding and Innovative Contributions to the Fields of Computer and Information Science, 1995
26. Outstanding Alumnus Award of UCLA Computer Science Department, 1994
27. Fellow of the Association for Computing Machinery (ACM), 1994
28. Member of the National Academy of Engineering, 1993
29. Karl V. Karlstrom Outstanding Educator Award, ACM, 1991
30. Fellow of the IEEE, 1989
31. Corporate Fellow, Thinking Machines Corporation, 1989
32. Senior Member of the IEEE, 1988
33. Distinguished Teaching Award, Berkeley Division of the U.C. Academic Senate and the California Alumni Association, 1982
34. U.C. Regents Summer Faculty Fellowship, 1977
35. Hughes Full-time Doctoral Fellowship 1974-75, 1975-76

Advisory Boards

1. SUN, Technical Advisory Board Member and Consultant
2. Microsoft, Consultant to Research and Board Member on ALT-Technical Advisory
3. Trustworthy Computing Academic Advisory Board
4. Sun Microsystems, Member of Technical Advisory Board, 2005-present
5. Microsoft, Member of Trustworthy Computing Academic Advisory Board, 2003-present
6. Microsoft Research, Member of Advisory Board, 2007-present
7. Kazaeon, Member of Technical Advisory Board, 2003-2007
8. Azul Systems, Member of Technical Advisory Board, 2003-2005
9. Sanera, Member of Technical Advisory Board, 2000-2003
10. Scale8, Chief Scientist and Member of Technical Advisory Board, 1999-2003

11. Data General Corporation, Member of the Scientific Advisory Board, 1984-1992

Consulting

- 1/84 to 2010: Sun Microsystems Inc. (computer architecture strategic directions)
- 5/88 to 6/93: Thinking Machines Corporation (massively parallel computer systems)
- 7/82 to 6/84: Xerox Palo Alto Research Center (computer architectures for Smalltalk)
- 7/80 to 6/82: Intel Corporation (VLSI computer architectures)
- 1/80 to 6/80: Digital Equipment Corp., Distributed and Mid-Range Advanced Systems Dev., consulting on higher level microprogramming tools for VAX computers
- 3/78 to 6/79: Hewlett-Packard, General Systems Division
Providing tools to move operating systems functions into microcode

Keynote Addresses and Distinguished Lectures

1. Keynote Speaker: “An Overview of Research at the UCB Par Lab”, VECPAR'10 High Performance Computing for Computational Science, June 2010.
2. Keynote Speaker: “Cloud Computing & RAD Lab”, Microsoft Cloud Futures, Redmond, April 2010.
3. Keynote Speaker: “Software Knows Best”, WOSP/SIPEW International Conference on Performance Engineering, January 2010.
4. Keynote Speaker: “The Parallel Revolution Has Started”, EE Times Virtual Conference, October 2009.
5. Keynote Speaker: “The Parallel Revolution Has Started”, Nokia Lab, CITRIS, October 2009.
6. Keynote Speaker: “Applying Machine Learning to Systems Management”, The IEEE International Conference on Self-Adaptive and Self-Organizing Systems (SASO 2009), San Francisco CA, September 2009.
7. Keynote Speaker: “The Parallel Revolution Has Started”, The International Conference on Microelectronic Systems Education (MSE), July 2009.
8. Keynote Speaker: ““The Parallel Revolution Has Started: Are You Part of the Solution or Part of the Problem?”, The International Conference on Microelectronic Systems Education (MSE), San Francisco CA, July 2009.
9. Distinguished Lecture: “The Parallel Revolution Has Started: Are You Part of the Solution or Part of the Problem?”, Triangle Lecture Series (Duke, USC, and NCU Universities), April 2009.
10. Distinguished Lecture: “The future of Parallel Programming”, University of Illinois @ Parallel Laboratory, April 2009.
11. Keynote Speaker: “Making a Difference – Leadership Through Innovation”, HKN General Meeting, November 2008.
12. Distinguished Lecture: “The Parallel Computing Landscape: A Berkeley View 2.0”, Supercomputing Workshop, November 2008.
13. Keynote Speaker: "The Parallel Computing Landscape: A Berkeley View 2.0", World Comp 08, Las Vegas NV, July 2008.
14. Distinguished Lecture: “Grand Challenges and Great Teams”, Eckert-Mauchly Award, Beijing, China, June 2008.

15. Keynote Speaker: "The Parallel Revolution Has Started: Are You Part of the Solution or Part of the Problem?", USENIX Conference, Boston MA, June 2008.
16. Keynote Speaker: "Towards a Declarative Datacenter with a Scalable Store." Microsoft Distributed Computing and Datacenter Seminar, May 2008.
17. Distinguished Lecture: "The Parallel Computing Landscape: A Berkeley View 2.0" AMD Distinguished Speaker Series, May 2008.
18. Distinguished Lecture: "Towards a Declarative Datacenter with a Scalable Store" Stanford's Clean Slate Seminar, Palo Alto CA, May 2008.
19. Distinguished Lecture: "Has Utility Computing Business Model Been Found (Finally)?" Sun Microsystems, March 2008.
20. Distinguished Lecture: "Towards a Declarative Datacenter with a Scalable Store," Distributed Computing and Storage Workshop, Microsoft, Redmond WA, January 2008.
21. Distinguished Lecture: "The Landscape of Parallel Computing Research: A View From Berkeley", University of Waterloo, Waterloo, Ontario Canada, October 2007.
22. Distinguished Lecture: "The Landscape of Parallel Computing Research: A View From Berkeley", IBM T.J. Watson, Yorktown Heights NY, October 2007.
23. Keynote Speaker: "The Parallel Computing Landscape: A Berkeley View", ISLPED (the Intl Symposium on Low Power Electronics & Design), August 2007.
24. Distinguished Lecture: "Longevity, its widespread use, and N2 performance on parallel kernels", SPARC, August 2007.
25. Keynote Speaker: "The Landscape of Parallel Computing Research: A View from Berkeley", 2.0, Manycore Computing Workshop, June 2007.
26. Distinguished Lecture, "The Berkeley View: A New Framework and a New Platform for Parallel Research," Carnegie Mellon University, Pittsburgh PA, March 2007.
27. Distinguished Lecture, "The Berkeley View: A New Framework and a New Platform for Parallel Research," Tokyo University of Technology, Tokyo Japan, March 2007.
28. Distinguished Lecture: "The Berkeley View: A New Framework and a New Platform for Parallel Research," PARC Forum, Palo Alto CA, November 2006.
29. Keynote Speaker: "The Berkeley View: A New Framework and a New Platform for Parallel Research," LCPC New Orleans, November 2006.
30. Keynote Speaker: "Creating a Thriving Manufacturing Base in 21st Century America", Association of Manufacturing Technology Annual Meeting, Lake Las Vegas NV, October 2006.

31. Keynote Speaker: "The RADical Approach to Next-Generation Information Services: Reliable, Adaptive, Distributed," ACM SIGMOD/PODS 2006 Conference, Chicago Illinois, June 2006.
32. Keynote Speaker: "Time to make concurrency RAMPant: a community vision for a shared experimental parallel hardware-software platform," Multicore Expo, Santa Clara California, March 2006.
33. Keynote Speaker: "RAMP: Research Accelerator for Multiple Processors - A Community Vision for a Shared Experimental Parallel HW/SW Platform," IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS-2006), Austin Texas, March 2006.
34. Keynote Address: "RAMP: Research Accelerator for Multiple Processors," WARFP-2006: 2nd Workshop on Architecture Research using FPGA Platform, Austin TX, February 2006.
35. Samuel D. Conte Distinguished Lecture: "How to Hurt Scientific Productivity," Purdue University, February 2006.
36. Keynote Address: "Making Concurrency RAMPant," IBM Almaden Academy Conference, San Jose CA, February 2006.
37. Keynote Address "+30 Years of IBM / U.C. Berkeley Synergy in Research," IBM Almaden Research 60th Anniversary Conference, San Jose CA, October 2005.
38. Keynote Address: "Why Latency Lags Bandwidth, and What It Means to Computing," 8th High Performance Embedded Computing Conference, MITRE, Boston, MA, September 2004.
39. Keynote Speaker: "Latency Lags Bandwidth," IBM Almaden Academy Performance Engineering Best Practices Conference, San Jose, California, June 2004.
40. Distinguished Lecturer: "Recovery Oriented Computing," Louisiana State University, February 2004.
41. Distinguished Lecturer: "Future Directions of Computer Engineering," William A. Anthony Lecture, Cornell University, November 2003.
42. Distinguished Lecturer: "Recovery Oriented Computing" Texas A&M University, October 2003.
43. Distinguished Lecturer: Organick Memorial Lecture, University of Utah, April 2003.
44. Distinguished Lecturer: State University of New York at Stony Brook, Stony Brook NY, February 2003.

45. Distinguished Lecturer: Santa Clara University, Santa Clara CA, January 2003.
46. Distinguished Lecturer: University of Michigan, Ann Arbor, MI, November 2002.
47. Distinguished Lecturer: "Recovery Oriented Computing (ROC)"; University of Illinois, Champaign-Urbana IL, October 2002.
48. Keynote speaker: "Embracing Failure: Recovery Oriented Computing (ROC)." Conference on High Speed Computing 02, Glendon Beach OR, April 2002.
49. Keynote speaker: "Recovery Oriented Computing: A New Research Agenda for a New Century." 8th International Symposium on High-Performance Computer Architecture (HPCA 8), Boston MA, February 2002.
50. Keynote speaker: "Availability and Maintainability Performance: New Focus for a New Century." USENIX FAST 02 Conference on File and Storage Technologies, Monterey CA, January 2002.
51. Keynote speaker: "Embracing Failure: A Case for Repair Centric Design." High Performance Transaction Systems Workshop, Asilomar CA, October 2001.
52. Keynote Speaker: 2001 International Parallel and Distributed Processing Symposium, San Francisco CA, April 2001.
53. Distinguished Lecturer: "Computers for the PostPC Era." University of Utah, March 2001.
54. Distinguished Lecturer: "Computers for the PostPC Era." University of California, Davis, November 2000.
55. Distinguished Lecturer: "Computers for the PostPC Era." University of Wisconsin, Madison, October 2000.
56. Distinguished Lecturer: "Computers for the PostPC Era." University of Virginia, October 1999.
57. Distinguished Lecturer: "Computers for the PostPC Era." University of Oregon, 1999.
58. Keynote Speaker: The ACM SIGMOD International Conference on Management of Data, 1998.
59. Distinguished Lecturer, Stanford Computer Science Colloquium, 1998.
60. Keynote Speaker: The Second International Workshop on SCI-based Low-cost/High-performance Computing (SCIzzl-2), 1995.
61. Keynote Speaker: The ACM SIGMETRICS Conference, 1993.

62. Keynote Speaker: The International Conference On Parallel And Distributed Information Systems, 1993.
63. Keynote Speaker: The Programmed Logic Devices Design Conference, 1993.
64. Distinguished Lecturer: National Academy of Science (Beijing, China), 1994.
65. Distinguished Lecturer, Cornell University, 1993.
66. Distinguished Lecturer, University of Arizona, 1993.
67. Distinguished Lecturer, University of Maryland, 1993.
68. Distinguished Lecturer, University of Minnesota, 1993.
69. Distinguished Lecturer, University of Wisconsin. 1993.
70. Distinguished Lecturer, Georgia Institute of Technology, 1992.
71. Distinguished Lecturer, University of Chicago, 1992.
72. Distinguished Lecturer, University of Texas, 1992.
73. Distinguished Lecturer, Carnegie-Mellon University, 1992.
74. Distinguished Lecturer, Duke University, 1992.
75. Distinguished Lecturer, Massachusetts Institute of Technology, 1992.
76. Distinguished Lecturer, Princeton, 1992.
77. Distinguished Lecturer, University of Southern California, 1992.
78. Distinguished Speaker, Computer Forum Lecture Series, Stanford University, 1983.
79. ACM National Lecturer, 1981-82.
80. Distinguished Speaker: Computer Forum Lecture Series, Stanford University. 1980-81.
81. ACM National Lecturer, 1980-81.

University Service

Academic Senate, Committee on Teaching, 2008-2010

Founding Director, Parallel Computing Lab (Par Lab), 2007 to present

Founding Director, Reliable Adaptive Distributed Computing Lab (RAD Lab), 2005 to present

Academic Senate, Committee on Budget and Interdepartmental Relations, 2001 to 2004

Associate Chair of Computer Science Division, EECS Department, 1990 to 1993

Academic Senate, Committee on Teaching, 1986-1989

Professional Service via Leadership Positions

Elected as Member of Board of RH CACM, 2008 to present

CACM, C0-Chair, Research Highlights Board, 2008 to present

Past President, Association for Computing Machinery, 2006 to 2008

Member, President's Council of Advisors on Science and Technology (PCAST), Technical Advisory Group on Networking and Information Technology, 2006 to present

Elected as President of the Association for Computing Machinery, 2004 to 2006

Member, President's Information Technology Advisory Committee (PITAC), 2003 to 2005

Member, Committee on Membership of the National Academy of Engineering, 2003 to 2006

Member, Computer Science and Telecommunications Board, National Research Council, 1998-2003

Elected as Chair of the Computing Research Association, 1993 to 1997

Elected as Member of Board of the Computing Research Association, 1991 to 2003

Elected as Chair of the ACM Special Interest Group in Computer Architecture, 1993 to 1995

Program Co-Chair, IEEE HOT chips Symposium IV, August 9-11, 1992, Stanford, CA

Chair of the Program Committee, Fourth Symposium on Architectural Support for Operating Systems and Computer Architecture, Santa Clara, CA., April, 1991

Elected as Director, ACM Special Interest Group on Computer Architecture, 1987 to 91

Guest Editor, Special Issue on Computer Architecture, Communications of the Association for Computing Machinery (CACM), January 1985

Editor, Journal of VLSI and Computer Systems, 1986 to 88

Chair of the 1982 ACM-IEEE Eckert-Mauchly Award Committee (Lifetime Award - Computer Architecture)

Professional Services via Committee Memberships

- CACM RH Board Committee Member
- Communications of the ACM, Co-Chair, Research Highlights Editorial Board
- Gigascale Systems Research Center (GSRC), Advisory Board
- Member of the Program Committee, The 32nd Annual International Symposium on Computer Architecture (ISCA) 2005
- Member of the Program Committee, The 3rd USENIX Conference of File and Systems Technologies (FAST'04) 2004
- Member of the Program Committee, The 30th Annual International Symposium on Computer Architecture (ISCA) 2003
- Member of the Program Committee, The 1st USENIX Conference of File and Systems Technologies (FAST'02) 2002
- Member, IEEE Von Neumann Medal Committee, 2002 to 2004
- Member, IEEE Mulligan Education Medal Committee, 2002 to 2003
- Member, Computing Research Association Awards Committee, 2001
- Member, Computing Research Association Government Affairs Committee, 2001
- Member of the Program Committee, Fifth International Symposium on High Performance Computer Architecture, 1999
- Member of the Program Committee, The 24th Annual International Symposium on Computer Architecture, 1997
- Member of the Program Committee, 21st Annual ACM-IEEE International Symposium on Computer Architecture, Chicago, Illinois, April, 1994
- Member, ACM Distinguished Fellow Committee, New York, 1993
- Member, Evaluation Committee, Graduate Program in Dept. of Computer Science &

Engineering, University of California at San Diego, 1992

- Member, Committee to evaluate computer science research and higher education at Norwegian universities (Bergen, Oslo, Trondheim and Tromsø), for NAVF: The Norwegian Council for Natural Science Research, 1992
- Member, NASA Management Operations Working Group (MOWG), Washington, D.C., 1992 to 1994
- Member of the Editorial Board, IEEE Spectrum, 1990 to 1992
- Member, Committee to Study the Scope and Role of Computer Science, National Academy of Sciences, 1990 to 1992
- Member of the Program Committee, 18th Annual ACM-IEEE International Symposium on Computer Architecture, Toronto, Canada, June, 1991
- Member of Program Committee, IEEE HOT chips Symposium II, August 20-21 1990, Santa Clara, CA
- Member of the Program Committee, 17th Annual ACM-IEEE International Symposium on Computer Architecture, Seattle, WA., May, 1990
- Member of the Program Committee, Third Symposium on Architectural Support for Operating Systems and Computer Architecture, Boston, MA. , April, 1989
- Member of the Program Committee, 31st Annual International Solid-State Circuits Conference, (ISSCC '84), February, 1984, San Francisco
- Member of the Program Committee and Session Chair, 22nd Annual *IEEE Computer* Conference (Spring COMPCON '84), February, 1984, San Francisco
- Member of the Program Committee, Session Chair, and Panel Participant 30th Annual International Solid-State Circuits Conference, (ISSCC '83), February, 1983, New York
- Member of the Program Committee and Session Chair, 21st Annual *IEEE Computer* Conference (Spring COMPCON '83), February, 1983, San Francisco
- Member of the Program Committee and Session Chair, 10th ACM-IEEE International Symposium on Computer Architecture, June 13-16, 1983, Stockholm, Sweden
- Member of the Program Committee, Session Chair, and Panel Participant; 1st Symposium on Architectural Support for Programming Languages and Operating Systems, March 1-3, 1982, Palo Alto, California
- Member of the Program Committee, Session Chair, and Panel Participant; 9th International Symposium on Computer Architecture, April 26-29, 1982, Austin, Texas

- Member of the Program Committee, 9th Annual Workshop on Microprocessors, April 21-23, 1982, Pacific Grove, California
- Member of the Program Committee, 20th Annual *IEEE Computer* Conference (Spring COMPCON '82), February 23-25, 1982, San Francisco
- ACM Representative of the Eckert-Mauchly Award Committee (Lifetime Award for Contributions Computer Architecture), 1980-1981
- Member of the Program Committee, 8th Annual Workshop on Microprocessors, April 21-23, 1981, Pacific Grove, California
- Member of the IEEE Subcommittee on a Microcomputer Standard for Binary Floating-Point Arithmetic, 1978-1980
- Member of the Program Committee, 19th *IEEE Computer Society International* Conference (Spring COMPCON '81), February 23-25, 1981, San Francisco, California
- Member of the Program Committee, 7th Asilomar Workshop on Microcomputers, April 23-25, 1980, Pacific Grove, California
- Member of the Program Committee, 6th Asilomar Workshop on Microcomputers, May 23-25, 1979, Pacific Grove, California
- Member of the Program Committee, International Symposium on Mini- and Microcomputers, January 15-18, 1979, Anaheim, California
- Member of the Steering Committee, Session Chair, and Publications Chair; 11th Workshop on Microprogramming, November 19-22, 1978, Pacific Grove, California

Books

1. *Computer Architecture: A Quantitative Approach, 4th Edition*, David A. Patterson and John L. Hennessy, 2008
2. *Computer Organization and Design: The Hardware/Software Interface, Fourth Edition*, John L. Hennessy and David A. Patterson, 2007
3. *Computer Architecture: A Quantitative Approach, Third Edition*, David A. Patterson and John L. Hennessy, 2003
4. *Information Technology for Counterterrorism: Immediate Actions and Future Possibilities*, John L. Hennessy, David A. Patterson, and Herbert S. Lin, Editors. Committee on the Role of Information Technology in Responding to Terrorism, National Research Council. National Academies Press, 2003
5. *Computer Architecture: A Quantitative Approach, Instructor's Manual*, Morgan Kaufmann Publishers, Inc., San Mateo, Ca., 1990, 210 pages. David A. Patterson and John L. Hennessy
6. *Computing Unbound: Using Computers in the Arts and Sciences*, David A. Patterson, Denise S. Kiser, D. Neel Smith, 1989
7. *Instructor's Manual for Computing Unbound*, David A. Patterson, Denise S. Kiser, 1989
8. *Computing Unbound: Hands-On Exercises for the Macintosh*, W.W. Norton & Company, New York, 1989, 307 pages. David A. Patterson, D.S. Kiser and D.N. Smith
9. *A Taste of Smalltalk*, Ted Kaehler and David A. Patterson, 1986

Chapters of Books

1. "RISC: Effective Architecture for VLSI Computers," *VLSI Design and Architecture*, vol. 14 of VLSI Electronics Microstructure Science, Academic Press, 1986, pp. 36-77. David Patterson, M.G.H. Katevenis, R.W. Sherburne, and C.H.
2. "Berkeley Smalltalk: Who Knows Where the Time Goes?" pp. 189-206 in *Smalltalk 80: Bits of History, Words of Advice*. (Edited by Glenn Krasner) Reading, MA: Addison-Wesley, 1983. David Patterson, D.M. Ungar

Articles for the Public

1. "An Endless Frontier Postponed," *Science* 6 May 2005, Vol. 308. no. 5723, p. 757 (Lazowska, Edward D. and Patterson, David A.)
2. "Surrendering U.S. Leadership in IT," (editorial), C/Net News.com, 5/11/2005, (David Patterson)

3. "Self Repairing Computers," *Scientific American*, June 2003. (Fox, Armando and Patterson, David A.)
4. "Vulnerable Intel (FTC files antitrust suit against chip maker)," (Column) *New York Times* v147 (June 9, 1998):A27(N), A21(L). (Patterson, David)
5. "Microprocessors in 2020," *Scientific American*, v273, n3 (Sept, 1995):62 (4 pages). (Patterson, David A.)
6. "Perspective on Education: If UC Slips, All of California Slips," *Los Angeles Times*, Op-Ed Page, Thursday, May 20, 1993, page B7. (Patterson, David)
7. "Teraflops Design in Eight Easy Steps," *Scientific American*, v264, n1 (January, 1991) pp. 104-105. (Patterson, David A.)
8. "Microprogramming, " *Scientific American*, vol. 248, No. 3, March, 1983, pp. 36-43.
9. "RISC: New Microcomputer Architecture Concept Implemented in Silicon by EECS, " Highlights in EECS, EECS/ERL Research Summary, 1982. David Patterson and C.H. Séquin.
10. "RISC y Course Sequence Creates RISCy Microcomputer, " Research Highlight, *Forefront*, Research in the College of Engineering, 1980-1981, pp. 14-18. David Patterson and C.H. Séquin.

Articles for the Information Technology Community

- "Stop whining about outsourcing!," *ACM Queue*, vol.3, no.9, Nov. 2005, pp. 63-4. (Patterson, D A)
- "Computing research: a looming crisis," *Computer Communication Review* 35(3): 65-68 (2005) (Lazowska, Edward D. and Patterson, David A.)
- "Farewell address: the growing and graying of ACM," *CACM* 49(6): 15-18 , 2006 (Patterson, D A)
- "Revisiting my campaign statement," *CACM* 49(5): 27-30 , 2006 (Patterson, D A)
- "Seven reasons to shave your head and three reasons not to: the bald truth," *CACM* 49(4): 31-32 , 2006 (Patterson, D A)
- "Computer science education in the 21st century," *CACM* 49(3): 27-30, 2006 (Patterson,D)
- "Reviving your favorite CS books," *CACM* 49(3): 31 , 2006 (Patterson, D A)
- "Offshoring: finally facts vs. folklore," *CACM* 49(2): 41-42 , 2006 (Patterson, D A)
- "New Directions for CACM?" *CACM* 49(1): 33-35 , 2006 (Patterson, D A)

“Robots in the desert: a research parable for our times,” *CACM* 48(12): 31-33 , 2005 (Patterson, D A)

“Rescuing our families, our neighbors, and ourselves,” *CACM* 48(11): 29-31 , 2005 (Patterson, D A)

“The new Professional Development Centre boasts 1, 000 courses, O'Reilly Books, and CS classics,” *CACM* 48(10): 15-16 , 2005 (Patterson, D A)

“Restoring the popularity of computer science,” *CACM* 48(9): 25-28 , 2005 (Patterson, D)

“Does ACM support matter to conferences or journals?” *CACM* 48(8): 29-30 , 2005 (Patterson, D A)

“Reflections on a programming Olympiad,” *CACM* 48(7): 15-16 , 2005 (Patterson, D A)

“Do you Queue?” *CACM* 48(6): 27-28 , 2005 (Patterson, D A)

“Recognizing individual excellence helps us all,” *CACM* 48(5): 27-28 , 2005 (Patterson, D A)

“The state of funding for new initiatives in computer science and engineering,” *CACM* 48(4): 21-25 , 2005 (Patterson, D A)

“20th century vs. 21st century C&C: the SPUR manifesto,” *CACM* 48(3): 15-16 , 2005 (Patterson, D A)

“Why join ACM?” *CACM* 48(2): 14 , 2005 (Patterson, D A)

“Minority-minority and minority-majority technology transfer,” *CACM* 48(1): 25-26 , 2005 (Patterson, D A)

“The health of research conferences and the dearth of big idea papers,” *CACM* 47(12): 23-24 , 2004 (Patterson, D A)

“Hot links,” *CACM* 47(10): 19-20 , 2004 (Patterson, D A)

“Fostering Community Within a CS&E Department: A Berkeley Perspective,” *Computing Research News*, Sept 2002. (Patterson, D A)

“Commercialization Oversight for Computer Research Departments,” *Computing Research News*, July 2001. (David Patterson and Larry Snyder)

“Evaluating Computer Scientists and Engineers For Promotion and Tenure,” *Computing Research News*, Sept 1999. (David Patterson, Lawrence Snyder, and Jeffrey Ullman)

“Has CS Changed in 20 Years?” *Computing Research News*, 4(2):2-3, March 1992. (Patterson, DA)

“EXPERT OPINION: Traditional mainframes and supercomputers are losing the battle," *IEEE Spectrum*, Vol. 29, No. 1, p. 34, January 1992. (Patterson, DA)

Refereed Journal, Conference, and Workshop Papers

Detecting Large-Scale System Problems by Mining Console Logs. International Conference on Machine Learning (ICML) June 2010 (Invited Application Paper), Haifa, Israel. (Wei Xu, Ling Huang, Armando Fox, David Patterson, Michael Jordan).

“In Characterizing, modeling, and generating workload spikes for stateful services” , In Symposium on Cloud Computing (SOCC), June 2010, Indianapolis, IN. (Peter Bodik, Armando Fox, Michael Franklin, Michael Jordan, David A. Patterson).

“Ubiquitous Parallel Computing from Berkeley”, Illinois and Stanford. . IEEE Micro. 2010 (Bryan Catanzaro, Armando Fox, Kurt Keutzer, David A. Patterson, Bor-Yiing Su).

“RAMP Gold: An FPGA-based Architecture Simulator for Multiprocessors”. In: Design Automation Conference (DAC-2010), June 2010, Anaheim, CA. (Zhangxi Tan, Andrew Waterman, Rimas Avizienis, Yunsup Lee, Krste Asanovic, David A. Patterson).

"A Case for FAME: FPGA Architecture Model Execution", To appear, International Symposium on Computer Architecture (ISCA-2010), June 2010, Saint-Malo, France. (Zhangxi Tan, Andrew Waterman, Henry Cook, Sarah Bird, Krste Asanović, David A. Patterson).

“Statistics-Driven Workload Modeling for the Cloud”. Most Popular Paper Award. Workshop on Self-Managing Database Systems (SMDB), March 2010, Long Beach, CA. (Archana Ganapathi, Yanpei Chen, Armando Fox, Randy Katz, David A. Patterson).

“Statistics-Driven Workload Modeling for the Cloud”. In: Workshop on Self-Managing Database Systems (SMDB). March 2010, Long Beach, CA. (Archana Ganapathi , Yanpei Chen, Armando Fox, Randy Katz, David A. Patterson).

“An FPGA-based Simulator for Datacenter Networks.”, In: The Exascale Evaluation and Research Techniques Workshop (EXERT 2010), at the 15th International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS March 2010). Pittsburgh, PA. (Zhangxi Tan, Krste Asanović, David A. Patterson).

“Online system problem detection by mining patterns of console logs detection”. International Conference on Data Mining series, December 2009, Miami, FL. (Wei Xu, Ling Huang, David Patterson, Armando Fox, Michael Jordan).

Mining Console Logs for Large-Scale System Problem Detection. Symposium on Operating Systems Principles (SOSP), August 2009, Big Sky, MT (Wei Xu, Ling Huang, David Patterson, Armando Fox, Michael Jordan).

“Automatic Exploration of Datacenter Performance Regimes. Workshop on Automatic Control for Datacenters and Clouds”. Conference on Autonomic Computing and Communications (ICAC 2009) June 2009, Barcelona, Spain. (Peter Bodik, Rean Griffith, Charles Sutton, Armando Fox, Michael I. Jordan, David A. Patterson).

“Your students are your legacy”: Viewpoints table of contents, Vol. 52, Issue 3 (March 2009), Being Human in the Digital Age, pp. 30-33, Year of Publication: 2009, ISSN: 0001-0782

“SCADS: Scale-Independent Storage for Social Computing Applications,” in CIDR, 2009 (M. Armbrust, A. Fox, D. A. Patterson, N. Lanham, B. Trushkowsky, J. Trutna, and H. Oh).

“Stencil computation optimization and auto-tuning on state-of-the-art multicore architectures”, Conference on High Performance Networking and Computing, Proceedings of the 2008 ACM/IEEE conference Austin Texas, Article No. 4, 2008. ISBN: 978-1-4244-2835-9. IEEE Press, Piscataway, NJ, USA (Datta K, Murphy M, Volkov V, Williams S, Carter J, Olier L, Patterson D, Shalf J, Yelick K).

“Automatic exploration of datacenter performance regimes”, International Conference on Autonomic Computing, Proceedings of the 1st workshop on Automated control for datacenters and clouds, Barcelona Spain, pp. 1-6, 2008. ISBN: 978-1-60558-585-7 (Bodik P, Griffith R, Sutton C, Fox A, Jordan M, Patterson D).

“A Case for Machine Learning to Optimize Multicore Performance.” First USENIX Workshop on Hot Topics in Parallelism (HotPar09), Berkeley, CA March 2009. (Ganapathi A, Datta K, Fox A, Patterson D).

“Roofline: an insightful visual performance model for multicore architectures”, Communications of the AM archive, Volume 52, Issue 4 (April 2009), A Direct Path to Dependable Software, pp. 65-76, 2009, ISSN: 0001-0782, Publisher ACM, New York, NY, USA (Williams S, Waterman A, Patterson D).

“Design and implementation trade-offs for wide-area resource discovery”, ACM Transactions on Internet Technology (TOIT) archive, Volume 8, Issue 4 (September 2008) table of contents, Article No. 18, Year of Publication: 2008, ISSN: 1533-5399, New York, NY, USA (Albrecht J, Oppenheimer D, Vahdat A, Patterson D).

“The Parallel Computing Landscape: a Berkeley View” [International Symposium on Low Power Electronics and Design], Session: DVC and thermal management, August 2007, pp. 231-231, ISBN: 978-1-59593-709-4.

“Technical perspective: the data center is the computer” [Article] *Communications of the ACM: Special Issue on Breakthrough Research: A Preview of Things to Come*, vol. 51, no. 1, pp. 105-105, Jan. 2008., ISSN: 0001-0782.

“RAMP: Research Accelerator for Multiple Processors.” IEEE Micro, vol. 27, no. 2, March/April 2007, pp. 46-57. (Wawrzynek J, Patterson D, Oskin M, Lu Shih-Lien, Kozyrakis C, Hoe J. C, Chiou D, Asanovic K).

“Embracing and extending 20th-century instruction set architectures.” [Journal Paper] Computer, vol. 40, no. 4, April 2007, pp. 68-75. Publisher: IEEE Compu. Soc, USA (Gebis J, Patterson D).

“Windows XP Kernel Crash Analysis”. Proceedings of the 20th conference on Large Installation System Administration Conference – Vol. 20 LISA '06 – Publisher: USENIX Association (Ganapathi A, Ganapathi V, Patterson D).

“ RAMP: research accelerator for multiple processors - a community vision for a shared experimental parallel HW/SW platform. “ ISPASS 2006. *IEEE International Symposium on Performance Analysis of Systems Software* IEEE. 2006.

"Advanced Tools for Operators at Amazon.com," *Workshop on Hot Topics in Autonomic Computing (HotAC '06)*. Dublin, Ireland, June 2006 (Peter Bodik, Armando Fox, Michael I. Jordan, David Patterson, Ajit Banerjee, Ramesh Jagannathan, Tina Su, Shivaraj Tenginkai, Ben Turner, Jon Ingalls).

"Regulating Workload in J2EE Application Servers", *First International Workshop on Feedback Control Implementation and Design in Computing Systems and Networks*, April , 2006, Vancouver, Canada (Wei Xu, Zhangxi Tan Armando Fox and David Patterson).

“Control considerations for scalable event processing,” *Proceedings 16th IFIP/IEEE International Workshop on Distributed Systems: Operations and Management, DSOM 2005* (Lecture Notes in Computer Science Vol.3775). Springer-Verlag. 2005, pp. 233-44. Berlin, Germany. (Wei Xu, Hellerstein JL, Kramer B, Patterson D).

“Design and implementation tradeoffs for wide-area resource discovery,” *Proceedings. 14th IEEE International Symposium on High Performance Distributed Computing*. 2005, pp. 113-24. Piscataway, NJ, USA. (Oppenheimer D, Albrecht J, Patterson D, Vahdat A).

“Crash data collection: a Windows case study,” *Proceedings. 2005 International Conference on Dependable Systems and Networks*, 2005, pp. 280-5. (Ganapathi A, Patterson D.).

“Combining visualization and statistical analysis to improve operator confidence and efficiency for failure detection and localization,” *Proceedings. Second International Conference on Autonomic Computing*. 2005, pp. 89-100. (Bodik P, Friedman G, Biewald L, Levine H, Candea G, Patel K, Tolle G, Hui J, Fox A, Jordan MI, Patterson D.).

Recovery-oriented computing: building multitier dependability. *IEEE Computer*, vol.37, no.11, Nov. 2004, pp. 60-7. (Candea G, Brown AB, Fox A, Patterson D.).

“Latency lags bandwidth,” *Communications of the ACM*, vol.47, no.10, Oct. 2004, pp. 71-5. (Patterson D.).

"VIRAM1: A Media-Oriented Vector Processor with Embedded DRAM", *Proc. 41st Design Automation Conference, Student Design Contest*, San Diego, CA, June 2004 (J. Gebis, S. William, C. Kozyrakis, D. Patterson).

“Path-based failure and evolution management,” First Symposium on Networked Systems Design and Implementation (NSDI '04). USENIX Assoc. 2004, pp. 309-22. Berkeley, CA, (Chen MY, Accardi A, Kiciman E, Patterson D, Fox A, Brewer E.).

“Combining statistical monitoring and predictable recovery for self-management.” Workshop on Self-Healing Systems, p 49-53, October 2004 (Armando Fox, Emre Kiciman, David A. Patterson).

“Experience with Evaluating Human-Assisted Recovery Processes,” Proceedings of the 2004 International Conference on Dependable Systems and Networks, Florence, Italy, June 2004. (Brown, Aaron B.; Chung, Leonard; Kakes, William; Ling, Calvin and Patterson, David A.).

“Scalable, vector processors for embedded systems,” *IEEE Micro*, vol 23, no 6, Nov to Dec 2003, pp 36-45. (Kozyrakis, CE, Patterson, DA).

“Undo for Operators: Building an Undoable E-mail Store” *Proc. 2003 USENIX Annual Technical Conference*, San Antonio, TX, June 2003. Best Paper Award. (Brown, Aaron B. and Patterson, David A.).

“Overcoming the limitations of conventional vector processors” *Proceedings 30th Annual International Symposium on Computer Architecture*, pp 399-409. June 2003. (Kozyrakis, C., and Patterson, David A.).

“Why do Internet services fail, and what can be done about it?” *4th USENIX Symposium on Internet Technologies and Systems*, March, 2003. (Oppenheimer, David, Ganapathi, Archana, and Patterson, David A.).

“Vector Vs Superscalar and VLIW Architectures for Embedded Multimedia Benchmarks,” *Proc. 35th International Symposium on Microarchitecture*, Istanbul, Turkey, November 2002. (C. Kozyrakis, D. Patterson).

“Practical issues in dependability benchmarking,” *Second Workshop on Evaluating and Architecting System Dependability (EASY)*, October 2002. (Oppenheimer, David, Brown, Aaron B., Traupman, Jonathan, Broadwell, Pete, and Patterson, David A.).

“Architecture, operation, and dependability of large-scale Internet services: three case studies.” *IEEE Internet Computing special issue on Global Deployment of Data Centers*, September/October 2002. (David Oppenheimer and David A. Patterson.).

“Why do Internet services fail and what can be done about it?” *10th ACM SIGOPS European Workshop*, Saint-Emilion, France, September 2002. (Oppenheimer, D., Patterson, D.A.).

“Rewind, Repair, Replay: Three R's to Dependability,” *10th ACM SIGOPS European Workshop*, Saint-Emilion, France, September 2002. (Brown, A. and Patterson, D.A.).

“Studying and using failure data from large-scale Internet services.” *10th ACM SIGOPS European Workshop*, Saint-Emilion, France, September 2002. (Oppenheimer, D. and D. A. Patterson).

“A simple way to estimate the cost of downtime.” *16th Systems Administration Conference (LISA '02)*, 2002.

“Capturing the Human Component of Dependability in a Dependability Benchmark,” *2002 DSN Workshop on Dependability Benchmarking*, Washington, D.C., June 2002. (Brown, A., Chung, L.C., Patterson, D.A.).

“Lessons from the PSTN for Dependable Computing” *Proceedings of the 2002 Workshop on Self-Healing, Adaptive and Self-MANaged Systems (SHAMAN)*, New York, June 2002. (Enriquez, P., A. Brown, and D. A. Patterson).

ROC-1: Hardware Support for Recovery-Oriented Computing,” *IEEE Transactions on Computers*, vol. 51, no. 2, February 2002. (Oppenheimer, D., Brown, A., Beck, J., Hettena, D., Kuroda, J., Treuhaft, N., Patterson, D.A., and Yelick, K.).

“Hardware/compiler Codevelopment for an Embedded Media Processor,” *Proceedings of the IEEE*, vol. 89, no. 11, November 2001 (p. 1694-709). (C. Kozyrakis, D. Judd, J. Gebis, S. Williams, D. Patterson, K. Yelick)

“Embracing Failure: A Case for Recovery-Oriented Computing (ROC),” *2001 High Performance Transaction Processing Symposium*, Asilomar, CA, October 2001. (Brown, A., Patterson, D.A.).

“To Err is Human,” *Proceedings of the First Workshop on Evaluating and Architecting System dependability (EASY '01)*, Goteborg, Sweden, July 2001. (Brown, A. and Patterson, D.A.).

“The art of massive storage: a Web image archive,” *Computer*, vol.33, (no.11), *IEEE Computer Society*, Nov. 2000. p.22-8. (Talagala, N., Asami, S., Patterson, D., Futernick, B., Hart, D.).

"Exploiting On-Chip Memory Bandwidth in the VIRAM Compiler," *Second Workshop on Intelligent Memory Systems*, Cambridge, November 2000. (D. Judd, K. Yelick, C. Kozyraki, D. Martin, and D. Patterson).

"Vector IRAM: A Media-oriented Vector Processor with Embedded DRAM," *12th Hot Chips Conference*, Palo Alto, CA, August 2000. (C. Kozyrakis, J. Gebis, D. Martin, S. Williams, I. Mavroidis, S. Pope, D. Jones, D. Patterson, K. Yelick).

“Towards availability benchmarks: a case study of software RAID systems,” *Proceedings of the 2000 USENIX Annual Technical Conference*, San Diego, California, June 18-23 2000, p.263-76. (Brown, A., Patterson, D.).

“A retrospective on twelve years of LISA proceedings,” *Proceedings of LISA'99: 13th System Administration Conference and Exhibition*, Seattle, Washington, 7-12 Nov. 1999, *USENIX Association*, 1999, p.95-107. Best Student Paper. (Anderson, E., Patterson, D.).

“ISTORE: introspective storage for data-intensive network services,” *Proceedings of the Seventh Workshop on Hot Topics in Operating System*, Rio Rico, Arizona, 29-30 March 1999, *IEEE Computer Society*, 1999, p.32-7. (Brown, A., Oppenheimer, D., Keeton, K., Thomas, R., Kubiawicz, J., Patterson, D.).

“Virtual log based file systems for a programmable disk,” Operating Systems Review (special issue. Third Symposium on Operating Systems Design and Implementation, New Orleans, Louisiana, 22-25 Feb. 1999, ACM, Winter 1998. p.29-43. (Wang, R.Y., Anderson, T.E., Patterson, D.A.).

“Cluster I/O with River: Making the Fast Case Common,” Workshop on I/O in Parallel and Distributed Systems. Proceedings of the Sixth Workshop on I/O in Parallel and Distributed Systems, ACM, Pages 10-22, 1999. (Arpaci-Dusseau, Remzi H., Anderson, Eric, Treuhaft, Noah, Culler, David E., Hellerstein, Joseph M., Patterson, David, Yelick, Kathy).

“IRAM: a microprocessor for the post-PC era.” Proceedings of International Symposium on VLSI Technology Systems and Applications. Taipei, Taiwan. 8-10 June 1999.

“Designing a self-maintaining storage system” 16th IEEE Symposium on Mass Storage Systems in cooperation with the 7th NASA Goddard Conference on Mass Storage Systems and Technologies. Information-based Access to Storage: Foundation of Information Systems, San Diego, CA, USA, 15-18 March 1999. p.222-33. vii+276 pp. 28 references. (Asami, S., Talagala, N., Patterson, D.A.).

“Usage patterns of a Web-based image collection,” 16th IEEE Symposium on Mass Storage Systems in cooperation with the 7th NASA Goddard Conference on Mass Storage Systems and Technologies. Information-based Access to Storage: Foundation of Information Systems, San Diego, CA, USA, 15-18 March 1999 pp.203-14. vii+276 pp. (Talagala, N., Asami, S., Patterson, D.).

“The Case for Redundant Arrays of Inexpensive Disks (RAID)”, Proceedings ACM SIGMOD Conference, Chicago, IL, (May 1988). ACM SIGMOD 1998 "Test of Time Award" for the most influential paper to be published in the conference proceedings from ten years previously. (D. Patterson, G. Gibson, R. Katz.).

“A New Direction for Computer Architecture Research” COMPUTER, Nov, 1998, v31(n11):24+. (Kozyrakis, C.E., Patterson, D.A.).

“A Case for Intelligent Disks (IDISks),” ACM SIGMOD Record, Volume 27, Issue 3, Pages 42-52 (September 1998). (Keeton, Kimberly, Patterson, David A., Hellerstein, Joseph M.).

“Searching for the sorting record: experiences in tuning NOW-Sort,” Proceedings of the SIGMETRICS Symposium on Parallel and Distributed Tools, Welches, Oregon, 3-4 Aug. 1998, ACM 1998. p.124-33. (Arpaci-Dusseau, A.C., Arpaci-Nusseu, R.H., Culler, D.E., Hellerstein, J.M., Patterson, D.A.).

“Performance Characterization of a Quad Pentium Pro SMP using OLTP Workloads,” 25th Annual International Symposium of Computer Architecture, Barcelona, Spain, June 27-July 1, 1998. Los Alamitos, CA USA: IEEE Comput. Soc, 1998. p. 15-26. (Keeton, K., Patterson, D., Yong Quian He, Raphael, R., and Baker, W. E.).

“The Architectural Costs of Streaming I/O; A Comparison of Workstations, Clusters and SMP’s,” 1998 Fourth International Symposium on High-Performance Computer Architecture, Las Vegas,

NV, USA, 1-4 February, 1998. Los Alamitos, CA, USA; IEEE Comp. Soc. 1998.p. 15-26.
(Arpaci-Dusseau, R., Arpaci-Dusseau, A., Culler, D., Hellerstein, J., and Patterson, D.).

“A new voting based hardware data prefetch scheme,” Proceedings. Fourth International Conference on High-Performance Computing, Bangalore, India, 18-21 Dec. 1997. 1997. p.100-5. xxiii+539 pp. (Singh Manku, G., Prasad, M.R., Patterson, D.A.).

“Intelligent RAM (IRAM): The Industrial Setting, Applications, and Architectures”, Appears in Proceedings ICCD ‘97 International Conference on Computer Design, Austin, Texas, October 10-12 1997 and International Conference on Computer Design. VLSI in Computers and Processors, Austin, TX, USA 12-15 October 1997. Los Alamitos, CA USA: IEEE Comput. Soc., p. 2-7. (Patterson, D., Asanovic, K., Brown, A., Fromm, R., Golbus, J., Gribstad, B., Keeton, K., Kozyrakis, C., Martin, D., Perissakis, S., Thomas, R., Treuhaft, N., and Yelick, K.).

“Extensible, Scalable Monitoring for Clusters of Computers,” Eleventh Systems Administration Conference, San Diego, CA, USA, 26-31 October 1997. Berkeley, CA, USA: USENIX Assoc., 1997. (Anderson, E., Patterson, D.).

“Scalable Processors in the Billion Transistor: IRAM” Proceedings *IEEE Computer Special Issue: Future Microprocessors - How to use a Billion Transistors*, September 1997. (C. Kozyrakis, S. Perissakis, D. Patterson, T. Anderson, K. Asanovic, N. Cardwell, R. Fromm, J. Golbus, B. Gribstad, K. Keeton, R. Thomas, N. Treuhaft, K. Yelick).

“High-performance sorting on networks of workstations,” SIGMOD Record, vol.26, (no.2), June 1997. p.243-54. 36. (Arpaci-Dusseau, A.C., Arpaci-Dusseau, R.H., Culler, D.E., Hellerstein, J.M., Patterson, D.A.).

“Evaluation of Existing Architectures in IRAM Systems” Workshop on Mixing Logic and DRAM: Chips that Compute and Remember at ISCA ‘97, Denver, CO, 1 June 1997. (N. Bowman, N. Cardwell, C. Kozyrakis, C. Romer, H. Wang).

IRAM and SmartSIMM: Overcoming the I/O Bus Bottleneck” Workshop on Mixing Logic and DRAM: Chips that Compute and Remember at ISCA ‘97, Denver, CO, 1 June 1997. (D. Patterson, R. Arpaci-Dusseau, K. Keeton).

“The Energy Efficiency of IRAM Architectures,” 24th Annual International Symposium on Computer Architecture. ISCA ‘97 *Computer Architecture News*, May 1997, vol.25, (no.2): 327-37. (Fromm, R., Perissakis, S., Cardwell, N., Kozyrakis, C., and others).

“A Case for Intelligent DRAM: IRAM” *IEEE Micro*, March-April 1997, vol.17, (no.2): 34-44. (Patterson, D., Anderson, T., Cardwell, N., Fromm, R., and others).

“Intelligent RAM (IRAM): Chips that Remember and Compute,” 1997 IEEE International Solid-State Circuits Conference. Digest of Technical Papers. ISSCC. San Francisco, CA, Feb 6-8 1997). (Edited by: Wuorinen, J.H. New York, NY, USA: IEEE, 1997. p. 224-5. (Patterson, D., Anderson, T., Cardwell, N., Fromm, R., and others).

“LogP: A Practical Model of Parallel Computation,” *Communications of the ACM*, Nov. 1996, vol.39, (no.11):78-85. (Culler, D.E., Karp, R.M., Patterson, D., Sahay, A., and others).

“Serverless network file systems,” *ACM Transactions on Computer Systems*, vol.14, (no.1), Feb. 1996. p.41-79. 55 (Anderson, T.E., Dahlin, M.D., Neeffe, J.M., Patterson, D.A., Roselli, D.S., Wang, R.Y.).

“Storage systems for movies-on-demand video servers.” *Proceedings of the Fourteenth IEEE Symposium on Mass Storage Systems. Storage - At the Forefront of Information Infrastructures* . 11-14 Sept. 1995. pp.246-56. (Chervenak AL. Patterson DA. Katz RH.).

“The interaction of parallel and sequential workloads on a network of workstations.” *Performance Evaluation Review*, vol.23, (no.1), (1995 ACM SIGMETRICS Joint International Conference on Measurement and Modeling of Computer Systems. Ottawa, Ont., Canada, 15-19 May 1995.) May 1995. p.267-78. (Arpaci, R.H., Dusseau, A.C., Vahdat, A.M., Lui, L.T., Anderson, T.E., Patterson, D.A.).

“The Berkeley Networks of Workstations (NOW) Project.” *Digest of Papers. COMPCON '95. Technologies for the Information Superhighway. COMPCON '95. San Francisco, CA, USA, 5-9 March 1995.*) p.322-6. (Anderson, T.E., Culler, D.E., Patterson, D.A.).

“A Case for NOW (Networks of Workstations),” *IEEE Micro*, February 1995, vol. 15, (no.1):54-64. (T. Anderson, D. Culler, D. Patterson).

“Cooperative caching: using remote client memory to improve file system performance,” *Proceedings of the First USENIX Symposium on Operating Systems Design and Implementation (OSDI)*. USENIX Assoc. 1994, pp.267-80. 14-17 Nov. 1994. (Dahlin MD. Wang RY. Anderson TE. Patterson DA.).

Coding techniques for handling failures in large disk arrays. *Algorithmica*, vol.12, no.2-3, Aug.-Sept. 1994, pp.182-208. Germany. (Hellerstein L. Gibson GA. Karp RM. Katz RM. Patterson DA).

“Toward workload characterization of video server and digital library applications” in *Performance Evaluation Review*, vol.22, no.1, May 1994, pp.274-5. USA. 1994 ACM SIGMETRICS Conference on Measurement and Modeling of Computer Systems. Nashville, TN, USA. ACM. 16-20 May 1994. “Drapeau AL, Patterson DA, Katz RH).

“A quantitative analysis of cache policies for scalable network file systems,” *Performance Evaluation Review*, vol.22, no.1, May 1994, pp.150-60. USA. 1994 ACM SIGMETRICS Conference on Measurement and Modeling of Computer Systems. Nashville, TN, USA. ACM. 16-20 May 1994. (Dahlin MD. Mather CJ. Wang RY. Anderson TE. Patterson DA.).

“RAID-II: a high-bandwidth network file server” *Proceedings of 21 International Symposium on Computer Architecture*. Chicago, IL, USA. IEEE. ACM. 18-21 April 1994. (Drapeau AL. Shirriff KW. Hartman JH. Miller EL. Seshan S. Katz RH. Lutz K. Patterson DA. Lee EK. Chen PM. Gibson GA).

"A 32b NMOS Microprocessor with a Large Register File," originally published in 1984, reprinted in 1993 International Solid-State Circuits Conference, IEEE 1993 ISSCC Commemorative Supplement. (Robert W. Sherburne, Jr., Manolis G.H. Katevenis, David A. Patterson, and Carlo H. Séquin).

"Storage Performance--Metrics and Benchmarks," Special Issue of the Proceedings of the IEEE devoted to "Computer Performance Evaluation," Aug. 1993. (David Patterson, P. Chen)

"Performance and Design Evaluation of the RAID-II Storage Server," International Parallel Processing Symposium Workshop on I/O in Parallel Computer Systems, April 1993, invited for submission to the Journal of Distributed and Parallel Databases. (Peter M. Chen, Edward K. Lee, Ann L. Drapeau, Ken Lutz, Ethan L. Miller, Srinivasan Seshan, Ken Shirriff, David A. Patterson, Randy H. Katz),

"LogP: Towards a Realistic Model of Parallel Computation," 14th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming, San Diego, CA 1993. (D. Culler, R. Karp, A. Sahay, K.E. Schauser, E. Santos, R. Subramonian, T. von Eicken).

"Designing Disk Arrays for High Data Reliability," Journal of Parallel and Distributed Computing, Vol. 17, No. 1-2, January-February 1993, pp. 4-27. (G.A. Gibson, D.A. Patterson).

"A New Approach to I/O Performance Evaluation--Self-Scaling I/O Benchmarks, Predicted I/O Performance," Appears in Proceedings of the 1993 ACM SIGMETRICS Conference on Measurement and Modeling of Computer Systems, May 1993, pp. 1-12; and ACM Transactions on Computer Systems, Vol. 12, no. 4, Nov. 1994, pp 308-39. (Peter M. Chen, David A. Patterson).

"Tradeoffs in Supporting Two Page Sizes," Proceedings 19th Annual Intl. Symposium on Computer Architecture, Gold Coast, Australia, May 19-21, 1992, pp. 415-424. (Madhusudhan Talluri, Shing Kong, Mark D. Hill, and David Patterson),

"Evaluation of a Stall Cache: An Efficient Restricted On-chip Instruction Cache," Proceedings of the Hawaii International Conference on System Sciences (HICSS-25), Hawaii, Volume I, January 1992, pp. 405-415. (Krste Asanovic, Klaus Erik Schauser, David A. Patterson, and Edward H. Frank)

"Maximizing Performance in a Striped Disk Array," 17th Annual International Symposium on Computer Architecture, Seattle, Washington, May, 1990. (Co-authored with P. Chen)

"An Evaluation of Redundant Arrays of Disks using an Amdahl 5890," ACM SIGMETRICS Conference on Measurement and Modeling of Computer Systems, Boulder, Colorado, May, 1990. (Co-authored with P. Chen, G. Gibson, and R. Katz).

"Disk System Architectures for High Performance Computing," Proceedings of the IEEE, Vol. 77, No. 12, December, 1989. (Co-authored with G. Gibson and R. Katz)

"A VLSI Chip Set for a Multiprocessor Workstation - Part I: A RISC Microprocessor with Coprocessor Interface and Support for Symbolic Processing," IEEE Journal of Solid-State

Circuits, vol. 24, no. 6, pp. 1688-1698, December 1989. (Co-authored with D. Lee, S. Kong, et al).

"A VLSI Chip Set for a Multiprocessor Workstation - Part II: A Memory Management Unit and Cache Controller," IEEE Journal of Solid-State Circuits, vol. 24, no. 6, pp. 1699-1707, December 1989. (co-authored with D. Jeong, D. Wood, et al).

"Failure Correction Techniques for Large Disk Arrays," Third International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS III), Boston, MA, April 1989. (Co-authored with G. Gibson, L. Hellerstein, R. Karp, and R. Katz).

"Introduction to redundant arrays of inexpensive disks (RAID)." Digest of Papers. COMPCON Spring '89. Thirty-Fourth IEEE Computer Society International Conference: Intellectual Leverage 1989, pp.112-17. Washington, DC, USA. San Francisco, CA, USA. IEEE. 27 Feb.-3 March 1989. (Patterson DA. Chen P. Gibson G. Katz RH.).

"The Design of XPRS," 1988 VLDB Conference, Long Beach, CA, September 1988. (Co-authored with M. Stonebraker, R. Katz, and J. Ousterhout).

"A Case for Redundant Arrays of Inexpensive Disks (RAID)," ACM SIGMOD Conference, Chicago, Illinois, June 1-3, 1988. (Co-authored with G. Gibson and R.H. Katz).

"The SPUR cache controller chip." Southcon/88 Conference Record. 1988, pp.88-90. Los Angeles, CA, USA. Orlando, FL, USA. 8-10 March 1988. (Wood DA. Eggers SJ. Gibson GA. Deog-Kyoon Jeong. Katz RH. Patterson DA.).

"Fast Scientific Computation in CMOS VLSI Shared-Memory Multiprocessors," 1988 IEEE International Symposium on Circuits and Systems, December 16, 1987, Espoo, Finland. (Co-authored with B.K. Bose, P.M. Hansen, and C. Lee).

"Fast Multiply and Divide for a VLSI Floating-Point Unit," Eighth International Symposium on Computer Arithmetic, May, 1987. (Co-authored with B.K. Bose, L. Pei, and G.S. Taylor).

"What price Smalltalk?" Computer, vol.20, no.1, Jan. 1987, pp.67-74. (Ungar D., Patterson DA.).

"RISC: effective architectures for VLSI computers." VLSI electronics. Microstructure science. Vol.14. VLSI design. Academic Press. 1986, pp.35-79. Orlando, FL, USA. (Katevenis MGH. Séquin CH. Patterson DA. Sherburne RW Jr.).

"Design Decisions in SPUR," IEEE Computer, vol. 19, no. 11, pp. 8-22, November 1986. (Co-authored with M.D. Hill, S.J. Eggers, J.R. Larus, G.S. Taylor, G. Adams, B.K. Bose, G.A. Gibson, P.M. Hansen, J. Keller, S.I. Kong, C.G. Lee, D. Lee, J.M. Pendleton, S.A. Ritchie, D.A. Wood, B.G. Zorn, P.N. Hilfinger, D.A. Hodges, R.H. Katz and J.K. Ousterhout).

"A VLSI RISC multiprocessor workstation." Proceedings of the IEEE International Conference on Computer Design: VLSI in Computers. ICCD '86. 1986, pp.94-6. Washington, DC, USA. Port Chester, NY, USA. IEEE. 6-9 Oct. 1986. (Katz RH. Patterson DA.).

“A 32b Microprocessor for Smalltalk,” IEEE Journal of Solid-State Circuits, vol. SC-21, no. 5, pp. 741-749, October 1986. (Co-authored with J.M. Pendleton, S.I. Kong, E.W. Brown, F. Dunlap, C. Marino, D.M. Ungar, and D.A. Hodges).

“Evaluation of the SPUR Lisp Architecture,” Thirteenth International Symposium on Computer Architecture, June 3-5 1986, Tokyo, Japan. (co-authored with G.S. Taylor, P.N. Hilfinger, J.R. Larus, and B.G. Zorn).

“An In-Cache Address Translation Mechanism,” Thirteenth International Symposium on Computer Architecture, June 3-5 1986, Tokyo, Japan. (co-authored by D.A. Wood, S.J. Eggers, G. Gibson, M.D. Hill, J.M. Pendleton, S.A. Ritchie, G.S. Taylor, and R.H. Katz.).

“Reduced Instruction Set Computers,” *Communications of the ACM*, Special Issue on Computer Architecture, January, 1985

A 32-bit NMOS Microprocessor with a Large Register File,” *IEEE Journal of Solid State Circuits*, Vol. SC-19, No. 5, October 1984, pp. 682-689. (Co-authored with M.G.H. Katevenis, R.W. Sherburne, and C.H. Séquin).

“Architecture of SOAR: Smalltalk on a RISC,” Proc. 11th Symp. on Computer Architecture, Ann Arbor, MI, June 1984. Also appeared in *Advanced Microprocessors and High-Level Language Computer Architecture* (Edited by V. Milutnovic), IEEE Computer Society Press, Silver Spring, MD., 1986, pp. 180-189. (Co-authored with D. Ungar, R. Blau, P. Foley, and D. Samples).

“A 32b NMOS Microprocessor with a Large Register File,” Proc. 31st International Solid States Circuit Conference, San Francisco, February 23-25, 1984. (Co-authored with M.G.H. Katevenis, R.W. Sherburne, and C.H. Séquin).

“VLSI Systems Building: A Berkeley Perspective,” Proc., Conference on Advanced Research in VLSI, Cambridge, MA, January 23-25, 1984, pp. 84-91.

“The RISC II Micro-Architecture,” VLSI 83 Conference, Trondheim, Norway, (August 1983). (Co-authored with M.G.H. Katevenis, R.W. Sherburne, and C.H. Séquin) .

“Local memory in RISCs.” Proceedings IEEE International Conference on Computer Design: VLSI in Computers (ICCD '83). IEEE Comput. Soc. Press. 1983, pp.149-52. Silver Spring, MD, USA. Port Chester, NY, USA. IEEE. 31 Oct.-3 Nov. 1983. (Sherburne RW Jr. Katevenis MGH. Patterson DA. Séquin CH.).

“Architecture of a VLSI Instruction Cache for a RISC,” Tenth Annual International Symposium on Computer Architecture, Stockholm, Sweden, June 15-18, 1983, pp. 108-115. Also appeared in *Advanced Microprocessors and High-Level Language Computer Architecture* (Edited by V. Milutnovic), IEEE Computer Society Press, Silver Spring, MD., 1986, pp. 158-166. (Co-authored with Phil Garrison, Mark Hill, Dimitris Lioupis, Chris Nyberg, Tim Sippel, and Korbin Van Dyke).

“Assessing RISC's in a High-Level Language,” *IEEE Micro*, vol. 2, No. 4., November, 1982. Also appeared in *Microprocessors and Microcomputers*, Third Edition (Edited by J.T. Cain), *IEEE Computer Society Press*, Silver Spring, MD., 1984, pp. 171-182. (Co-authored with R.S. Piepho).

“Design and Implementation of RISC I,” *Proceedings Advanced Course on VLSI Architecture*, Univ. of Bristol, England (October 1982). Also appeared in *VLSI Architecture* (Edited by B. Randell and P.C. Treleaven) *Prentice Hall International*, Hertfordshire, England (June 1983). (Co-authored with C.H. Séquin).

“A performance evaluation of the Intel 80286.” *Computer Architecture News*, vol.10, no.5, Sept. 1982, pp.16-18. USA

“A VLSI RISC,” *Computer*, vol. 15, No. 9, September, 1982. Also appeared in *Microprocessors and Microcomputers*, Third Edition (Edited by J.T. Cain), *IEEE Computer Society Press*, Silver Spring, MD., 1984, pp. 158-170. Also appeared in *Advanced Microprocessors and High-Level Language Computer Architecture* (Edited by V. Milutinovic), *IEEE Computer Society Press*, Silver Spring, MD., 1986, pp. 145-157. (Co-authored with C.H. Séquin).

“RISC Assessment: A High-Level Language Experiment,” 9th Annual Symposium on Computer Architecture, Austin, Texas, April 26-29, 1982. pp. 1-6. (Co-authored with R.S. Piepho).

“A RISCy approach to computer design.” *Digest of Papers Spring COMPCON 82*. High Technology in the Information Industry. IEEE. 1982, pp.8-14. New York, NY, USA. San Francisco, CA, USA. IEEE. 22-25 Feb. 1982

“Datapath Design for RISC,” *Proc. Conference on Advanced Research in VLSI*, Cambridge, Massachusetts, January 25-27, 1982. (Co-authored with M.G.H. Katevenis, C.H. Séquin, and R.W. Sherburne)

“An Experiment In High Level Language Microprogramming and Verification,” *Communications of the ACM*, October 1981.

“VLSI Implementations of a Reduced Instruction Set Computer,” *Proceedings of the CMU Conference on VLSI Systems and Computations*, October 19-21, 1981, Pittsburgh Pennsylvania. (Co-authored with D.T. Fitzpatrick, J.K. Foderaro, M.G.H. Katevenis, H.A. Landman, J.B. Peek, Z. Peshkess, C.H. Séquin, R.W. Sherburne, and K.S. Van Dyke).

“VAX Hardware for the Proposed IEEE Floating Point Standard,” *Proc. 5th Symposium on Computer Arithmetic*, pp. 190-196, May 18-19, 1981, Ann Arbor, Michigan. (Co-authored with G. Taylor).

“RISC I: A Reduced Instruction Set VLSI Computer,” *Proc. 8th International Symposium on Computer Architecture*, pp. 443-457, May 12-14, 1981, Minneapolis, Minnesota. (Co-authored with C.H. Séquin).

“V-Compiler: A Next Generation Tool for Microprogramming,” *Proc. National Computer*

Conference, pp. 103-109, May 4-8, 1981, Chicago, Illinois. (co-authored with R. Goodell, M. Poe, and S. Steely).

"Retrospective on High-Level Language Computer Architecture," Conference Proceedings, 7th Annual International Symposium on Computer Architecture, pp. 97-104, May 6-8, 1980, La Baule, France. Also appeared in *Advanced Microprocessors and High-Level Language Computer Architecture* (Edited by V. Milutnovic), *IEEE Computer Society Press*, Silver Spring, MD., 1986, pp. 63-70. (Co-authored with D.R. Ditzel).

"Design Considerations for Single-Chip Computers of the Future," *IEEE Journal of Solid-State Circuits*, vol. SC-15, no. 1, pp. 44-52, *IEEE Transactions on Computers*, vol. C-29, no. 2, pp. 108-116, Joint Special Issue on Microprocessors and Microcomputers, February 1980. Also appeared in *Advanced Microprocessors*, (Edited by Amar Gupta and Hoo-min Toong) *IEEE Press*, New York, N.Y., 1983, pp. 269-276. (co-authored with C.H. Séquin).

"Towards an Efficient Machine-Independent Language for Microprogramming," 12th Annual Workshop on Microprogramming, pp. 22-35, November 18-21, 1979, Hershey, Pennsylvania. (co-authored with K. Lew and R.L. Tuck).

"Design Considerations for the VLSI Processor of X-TREE," 6th Annual International Symposium on Computer Architecture, April 23-25, 1979, Philadelphia, Pennsylvania. (co-authored with E.S. Fehr and C.H. Séquin).

"Communication in X-TREE, a Modular Multiprocessor System", *Proceedings of ACM National Conference*, Washington, D.C., Dec. 1978 (co-authored with A.M. Despain and C.H. Séquin).

"An Approach To Firmware Engineering," *Proceedings of the National Computer Conference*, June 5-8, 1978, Anaheim, California.

"X-TREE: A tree structured multiprocessor computer system," 5th Annual International Symposium on Computer Architecture, April 3-5, 1978, Palo Alto, California. (co-authored with A.M. Despain).

"Strum: Structured Microprogramming System For Correct Firmware," *IEEE Transactions on Computers*, Special Issue on Microprogramming, pp. 974-985, October, 1976, vol. C-25, no. 10.

"The Design of a System for the Synthesis of Correct Microprograms," *Proceedings of the 8th Annual Workshop on Microprogramming*, pp. 13-17, September 21-23, 1975.

Non-Refereed Magazine and Newsletter Articles, Conference Papers, and Reports

“A View of Cloud Computing”. Communications of the ACM, Volume 53, Number 4, Pages 50-58, March 2010. (Michael Armbrust, Armando Fox, Rean Griffith, Anthony D. Joseph, Randy Katz, Andy Konwinski, Gunho Lee, David Patterson, Ariel Rabkin, Ion Stoica, Matei Zaharia).

"The Manycore Revolution: Will the HPC Community Lead or Follow?", SciDAC Review, Fall 2009. (John Shalf, Krste Asanović, David Patterson, Kurt Keutzer, Tim Mattson, and Katherine Yelick).

"A View of the Parallel Computing Landscape," Communications of the ACM, vol. 52, Oct. 2009. (Krste Asanović, Rastislav Bodik, James Demmel, Tony Keaveny, Kurt Keutzer, Nelson Morgan, David A. Patterson, Koushik Sen, John Wawrzynek, David Wessel, and Kathy A. Yelick).

Roofline: an insightful visual performance model for multicore architectures, Communications of the ACM archive, Volume 52, Issue 4 (April 2009), A Direct Path to Dependable Software, Pages 65-76, Year of Publication: 2009, ISSN:0001-0782, Publisher ACM, New York, NY, USA (Samuel Williams, Andrew Waterman, David Patterson)

“Above the Clouds: A Berkeley View of Cloud Computing, EECS Department University of California, Berkeley Technical Report No. UCB/EECS-2009-28 February 10, 2009 (Armbrust M, Fox A, Griffith R, Joseph A, Katz R, Konwinski A, Lee G, Patterson D, Rabkin A, Stoica I, Zaharia M).

Design and implementation trade-offs for wide-area resource discovery, ACM Transactions on Internet Technology (TOIT) archive, Volume 8, Issue 4 (September 2008) table of contents, Article No. 18, Year of Publication: 2008, ISSN:1533-5399, Publisher - ACM New York, NY, USA ((Jeannie Albrecht, David Oppenheimer, Amin Vahdat, David Patterson)

"An FPGA Host-Multithreaded Functional Model for SPARC v8", 3rd Workshop on Architectural Research Prototyping (WARP-2008), at 35th International Symposium on Computer Architecture (ISCA-35), Beijing, China, June 2008 (Tan, Z., K. Asanovic, and David Patterson)

"The Parallel Computing Laboratory at U.C. Berkeley: A Research Agenda Based on the Berkeley View," EECS Department, University of California, Berkeley, Tech. Rep. UCB/EECS-2008-23, March 2008 (K. Asanovic, R. Bodik, J. Demmel, T. Keaveny, K. Keutzer, J. D. Kubiatowicz, E. A. Lee, N. Morgan, G. Necula, D. A. Patterson, K. Sen, J. Wawrzynek, D. Wessel, and K. A. Yelick)

David A. Patterson, “Technical perspective: the data center is the computer”[Article] Communications of the ACM: Special Issue on Breakthrough Research: A Preview of Things to Come, vol. 51, no. 1, pp. 105-105, Jan. 2008., ISSN: 0001-0782

“The Parallel Computing Landscape: a Berkeley View” [International Symposium on Low Power Electronics and Design], Session: DVC and thermal management, August 2007, pp 231-231, ISBN: 978-1-59593-709-4 (David Patterson)

“The Landscape of Parallel Computing Research: A View from Berkeley,” EECS Department, University of California, Berkeley, Tech. Rep. UCB/EECS-2006-183, Dec. 2006. (K. Asanovic, R. Bodik, B. C. Catanzaro, J. J. Gebis, P. Husbands, K. Keutzer, D. A. Patterson, W. L. Plishker, J. Shalf, S. W. Williams, and K. A. Yelick)

“RAMP: A Research Accelerator for Multiple Processors.” EECS Department, University of California, Berkeley, Tech. Rep. UCB/EECS-2006-158, Nov. 2006 (J. Wawrzynek, M. Oskin, C. Kozyrakis, D. Chiou, D. A. Patterson, S. Lu, J. C. Hoe, and K. Asanovic)

“RAMP: Research Accelerator for Multiple Processors - A Community Vision for a Shared Experimental Parallel HW/SW Platform,” EECS Department, University of California, Berkeley, Technical Report No. UCB/CSD-05-1412, 2005 (Arvind, Krste Asanovic, Derek Chiou, James C. Hoe, Christoforos Kozyrakis, Shih-Lien Lu, Mark Oskin, David Patterson, Jan Rabaey and John Wawrzynek)

Report to the President: Computational Science: Ensuring America's Competitiveness, President's Information Technology Advisory Committee, 117 pages, June 2005 (Marc Benioff, Edward Lazowska, Ruzena Bajcsy, J. Carter Beese, Jr., Pedro Celis, Patricia Thomas Evans, Manuel Fernandez, Luis Fiallo, José-Marie Griffiths, William Hannigan, Jonathan Javitt, Judith Klavans, F. Thomson Leighton, Harold Mortazavian, Randall Mott, Peter Neupert, Eli Noam, David Patterson, Alice Quintanilla, Daniel Reed, Eugene Spafford, David Staelin, Peter Tippet, and Geoffrey Yang)

Report to the President: Cyber Security: A Crisis of Prioritization, President's Information Technology Advisory Committee, 72 pages, February 2005 (Marc Benioff, Edward Lazowska, Ruzena Bajcsy, J. Carter Beese, Jr., Pedro Celis, Patricia Thomas Evans, Manuel Fernandez, Luis Fiallo, José-Marie Griffiths, William Hannigan, Jonathan Javitt, Judith Klavans, F. Thomson Leighton, Harold Mortazavian, Randall Mott, Peter Neupert, Eli Noam, David Patterson, Alice Quintanilla, Daniel Reed, Eugene Spafford, David Staelin, Peter Tippet, and Geoffrey Yang)

Report to the President: Revolutionizing Health Care Through Information Technology, President's Information Technology Advisory Committee, 60 pages, June 2004 (Marc Benioff, Edward Lazowska, Ruzena Bajcsy, J. Carter Beese, Jr., Pedro Celis, Patricia Thomas Evans, Manuel Fernandez, Luis Fiallo, José-Marie Griffiths, William Hannigan, Jonathan Javitt, Judith Klavans, F. Thomson Leighton, Harold Mortazavian, Randall Mott, Peter Neupert, Eli Noam, David Patterson, Alice Quintanilla, Daniel Reed, Eugene Spafford, David Staelin, Peter Tippet, and Geoffrey Yang)

“RAMP: research accelerator for multiple processors - a community vision for a shared experimental parallel HW/SW platform,” Abstract ISPASS 2006. IEEE International Symposium on Performance Analysis of Systems Software. 2006, Austin Texas, , pp. 1.. (Patterson DA.)

"Latency lags bandwidth," Abstract. Proceedings. 2005 International Conference on Computer Design . IEEE Comput. Soc. 2005, pp. 3.. (Patterson DA.)

"Guest Editors' Introduction: Approaches to Recovery-Oriented Computing,". *IEEE Internet Computing* 9(2): 14-16 (2005) (Armando Fox, David A. Patterson)

David Oppenheimer, Jeannie Albrecht, David Patterson and Amin Vahdat, "Scalable Wide-Area Resource Discovery," EECS Department, University of California, Berkeley, Technical Report No. UCB/CSD-04-1334, 2004

"Decentralized Systems Need Decentralized Benchmarks," EECS Department, University of California, Berkeley, Technical Report No. UCB/CSD-03-1234, 2003. (David Oppenheimer, David A. Patterson and Joseph M. Hellerstein)

"Interview: A Conversation with Jim Gray," *Queue*, 1(4), June 2003, 8-17. (Patterson, D.A.)

"Benchmarking DHTs with Queries," UC Berkeley Technical Report UCB//CSD-03-1222, January 2003, (David Oppenheimer, Joseph M. Hellerstein, Ryan Huebsch, and David A. Patterson.)

"Recovery Oriented Computing: A New Research Agenda for a New Century" Abstract *Proc. High Performance Computer Architecture* 2002: p 247 (Patterson DA.)

"Recovery-Oriented Computing (ROC): Motivation, Definition, Techniques, and Case Studies," UC Berkeley Computer Science Technical Report UCB//CSD-02-1175, March 15, 2002. Also submitted to the Tenth International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS-X), October 2002. Patterson, D. A., Brown, A., Broadwell, P., Candea, G., Chen, M., Cutler, J., Enriquez, P., Fox, A., Kiciman, E., Merzbacher, M., Oppenheimer, D., Sastry, N., Tetzlaff, W., Traupman, J., Treuhaft, N.

Looking Over the Fence at Networks: A Neighbor's View of Networking Research, edited by David A. Patterson, 36 pages, Computer Science Technology Board, National Academy Press, ISBN: 0-309-07613-7, 2001.

"Truth in SPEC benchmarks," *Computer Architecture News*, vol.23, (no.5), ACM, Dec. 1995. p.34-42. (Mirghafori, N., Jacoby, M., Patterson, D.)

"The Video Mentor Program: Helping New Faculty with Teaching," *Computing Research News*, Vol. 5, No. 2, March 1993, pp. 15-16. David A. Patterson.

Informatikk: Research and Teaching in Norway, A Critical Evaluation. NAVF-The Council for Natural Science Research, December 1992, 66 pages, Oslo, Norway. Erich J. Neuhold, Ake Bjork, Arto Salomaa, David Parnas, and David Patterson

Computing the Future: A Broader Agenda for Computer Science and Engineering. Committee to Assess the Scope and Direction of Computer Science and Technology, Computer Science and Telecommunications Board, Commission on Physical Sciences, Mathematics, and Applications, National Research Council. Washington, D.C., National Academy Press, July 1992. 272 pages.

Juris Hartmanis, Herbert Lin, Ruzena Bajcsy, Ashok K. Chandra, Andries Van Dam, Jeff Dozier, James Gray, David Gries, A. Nico Habermann, Robert R. Johnson, Leonard Kleinrock, M. Douglas McIlroy, David A. Patterson, Raj Reddy, Klaus Schulten, Charles Seitz, and Victor Vyssotsky

"Robo-line Storage: Low Latency, High Capacity Storage Systems Over Geographically Distributed Networks," UCB/CSD Tech. Report No. 92/651, 1992. R.H. Katz, T.E. Anderson, J.K. Ousterhout, D.A. Patterson.

"RAID-II: A Scalable Storage Architecture for High-Bandwidth Network File Service," UCB/CSD Tech. Report No. 92/672, 1992. E.K. Lee, P.M. Chen, J.H. Hartman, A.L. Chervenak Drapeau, E.L. Miller, R.H. Katz, G.A. Gibson, D.A. Patterson.

"An Approach to Cost-Effective Terabyte Memory Systems," Digest of Papers. COMPCON Spring 1992. Thirty-Seventh *IEEE Computer Society International Conference* (Cat. No.92CH3098-1). San Francisco, CA, USA, February 24-28, 1992. Los Alamitos, CA, USA: IEEE Comput. Soc. Press, 1992, pp. 395-400. . R.H. Katz, D.A. Patterson, A. Chervenak-Drapeau, J. Fine, et al,

"A project on high performance I/O subsystems," *Computer Architecture News*, Sept. 1989, vol.17, (no.5):24-31. Katz, R.H.; Ousterhout, J.K.; Patterson, D.A.; Chen, P.; and others.

"Introduction to RAIDs, '' Proceedings of Spring COMPCON Conference, San Francisco, CA, March 1989. (Co-authored with P. Chen, G. Gibson, and R. Katz)

"How Reliable is a RAID?" Proceedings of Spring COMPCON Conference, San Francisco, CA, March 1989. (Co-authored with M. Schulze, G. Gibson, and R. Katz)

"The Scalable Processor Architecture, '' COMPCON88, February, 1988. (Co-authored with R.B. Garner, A. Agrawal, F. Briggs, E. Brown, D. Hougitt, B. Joy, S. Kleiman, S. Muchnick, M. Namjoo, J. Pendleton, and R. Tuck)

"A Case for Redundant Arrays of Inexpensive Disks, '' Technical Report, UCB/CSD 87/391, Computer Science Division (EECS), University of California Berkeley, December, 1987. (co-authored with G. Gibson and R. Katz)

"Design Methodology of a VLSI Multiprocessor Workstation, '' VLSI Design Magazine, February, 1987. (Co-authored with S.I. Kong, D.A. Wood, G.A. Gibson and R.H. Katz)

"Everything You Always Wanted to Know About 'An Introduction to Computers'," Technical Report, UCB/CSD 86/287, Computer Science Division (EECS), University of California, Berkeley, March, 1986.

"SPUR: A VLSI Multiprocessor Workstation, '' Technical Report, UCB/CSD 86/273, Computer Science Division(EECS), University of California Berkeley, December, 1985. (co-authored with M.D. Hill, S.J. Eggers, J.R. Larus, G.S. Taylor, G. Adams, B.K. Bose, G.A. Gibson, P.M.

Hansen, J. Keller, S.I. Kong, C.G. Lee, D. Lee, J.M. Pendleton, S.A. Ritchie, D.A. Wood, B.G. Zorn, P.N. Hilfinger, D.A. Hodges, R.H. Katz, and J. Ousterhout)

"Berkeley Smalltalk Announcement," Smalltalk-80 Newsletter, No. 3, June 1984, pp. 2-5.

"RISC Watch," *Computer Architecture News*, vol. 12, no. 1, March 1984, pp. 11-19.

"Smalltalk on a VAX?" Smalltalk-80 Newsletter, No. 2, February 1984, pp. 3-4.

"Running RISC's," VLSI Design, vol. III, No. 5, September/October, 1982. (Co-authored with J.K. Foderaro and K.S. Van Dyke)

"A Performance Evaluation of the Intel iAPX 432," *Computer Architecture News*, vol. 10, no. 4, June 15, 1982, pp. 17-26. Also appeared in *Advanced Microprocessors*, (Edited by Amar Gupta and Hoo-min Toong) IEEE Press, New York, N.Y., 1983, pp. 242-252. (Co-authored with P. Hansen, A. Linton, R. Mayo, and M. Murphy)

"A RISCy Approach to Microprocessor Technology," 20th Annual *IEEE Computer Conference* (COMPCON '82), February 24-26, 1982, San Francisco.

"RISC: New Microcomputer Architecture Concept Implemented in Silicon by EECS," Highlights in EECS, EECS/ERL Research Summary, 1982. (Co-authored with C.H. Séquin).

"A RISCy Approach to VLSI," VLSI Design, Fourth Quarter (November 1981). Also appeared in *Computer Architecture News*, vol. 10, No. 2, 15 April 1982. (Co-authored with D.T. Fitzpatrick, J.K. Foderaro, M.G.H. Katevenis, H.A. Landman, J.B. Peek, Z. Peshkess, C.H. Séquin, R.W. Sherburne, and K.S. Van Dyke)

"Formal verification in an unverified environment: An experiment," Proceedings of the 6th Texas Conference on Computing, pp. 4A.13-4A.18, November 14-16, 1977, Austin, Texas.

"The Case for the Reduced Instruction Set Computer," *Computer Architecture News*, pp. 25-33, vol. 8, No. 6, 15 October 1980. (Co-authored with D.R. Ditzel)