Volume 1 Issue 3, August 2012

International Journal of Innovative Technology and Exploring Engineering



ISSN: 2278 - 3075

Website: www.ijitee.org





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N		lished By: Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd.	e		
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		Comparative Performance Analysis of ANN Implemented LMS with ANN for Channel Estim	nation		
	Paper Title: Comparative Performance Analysis of ANN Implemented Livis with ANN for Channel Estimate in AWGN Channel Scenario				
	Abstract: In this paper we have done channel estimation using the concepts of LMS algorithm, after that we have				
	implemented the logic of LMS algorithm using the concepts of Supervised Artificial Neural Network and then we have				
	performed channel estimation directly applying the concepts of Supervised Artificial Neural Network. Finally we have compared the performances (BER v/s SNR and Throughput v/s SNR) of these three methods for channel estimation under AWGN channel scenario. Matlab (version 7.9) is used here as the simulation platform.				
	Keywords: Channel estimation, LMS, Artificial Neural Network, BER, Throughput				
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	Paper Title:	A Security Solution for the Transmission of Confidential Data and Efficient File Authenti	cation		
	raper riue:	Based on DES, AES, DSS and RSA			

Data security is an integral part of web based business applications like insurance, banking etc. These Abstract: applications require a secure infrastructure to meet the security requirements of confidentiality, endpoint authentication, message integrity and no repudiation. Document encryption/decryption and signatures/validation are the data security standards that define XML vocabularies and processing rules to meet these security requirements.

In this paper, we present a how a file securely passes from sender (server) to the receiver (client) through a central gateway with web services applications which mean a secure architecture for the exchange of confidential documents. Designing a secured electronic system architecture i.e. connected to the central gateway through which the whole work is established and takes place accordingly to pass only the files over the internet and have security features like digital signatures. Various algorithms, implementations and coding have been developed for encryption/decryption, signatures/validations and web services.

Keywords: Confidentiality, Authentication, Algorithms and Methods.

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Authors:	Zhenxing Luo
Paper Title:	Robust Energy-based Target Localization in Wireless Sensor Networks in the Presence of Byzantine Attacks

Abstract: This paper presents a robust energy-based target localization method in wireless sensor networks (WSNs) in the presence of Byzantine attacked sensors. Byzantine attacks will cause sensors to send false information to the fusion centre and disturb the target localization method. Therefore, a robust energy-based target localization method is needed to counter the Byzantine attacks and a method is presented in this paper. The method presented in this paper assumes that the fusion centre knows the exact information about the percentage of Byzantine sensors and the attack probability. If the fusion centre does not know the Byzantine attack information, a Byzantine sensor identification scheme can be used to identify Byzantine sensors. Results showed the robust energy-based target localization method could provide better performance results than the energy-based target localizatin method, which did not consider Byzantine attacked sensors. Moreover, simulation results showed that the Byzantine sensor identification scheme could identify most Byzantine attacked sensors.

Keywords: Byzantine sensors, Cramer-Rao lower bound, maximum likelihood estimation, reputation value.

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Authors: Srikanth V, Leelavathi G

Abstract: The preceding and existing video surveillance system which entails high end cameras, video servers, network switch and monitoring PC all these resources leads to complexity, expensive, high power consumption and also requires more area to establish. In order to overcome the hitch in the preceding and existing system, this paper presents a proficient where it uses few hardware resources for the implementation of the video monitoring system. S3C2440 is a very good ARM9 family processor providing a camera interface which is very conducive to the application and development. Embedded Linux is chosen as operating system which provides open-source, multi-task, multi-process, highly modular, multi-platform support, performance and stability to the system. The design system achieves maximum frame rate of 30fps with a resolution of 1280x1024 if individual camera is initialized and 10fps with a maximum video resolution of 340x480 if two cameras are initialized. The application of this paper can be implemented at security surveillance, patient monitoring in hospitals and polling booths.

Keywords: ARM9, MINI 2440, MJPG-streamer, JPG, IP address,

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Roopashree S S, Manjunath Lakkannavar **Authors: Paper Title:** The Controlling of Mobile Robot Based on ARM9

In this proposed work the robot is built using two dc motors for the movement which is administered by AT89C51 Microcontroller, the obstacle is detected by ultrasonic sensor, the controlling of the robot is done by using the resources of MINI 2440 development board. The application of this system can be implemented at archeological survey, place of natural disaster and industrial applications. The previous system of Mobile robot which relies on PC System to coordinate the whole resources, in order to achieve advanced arithmetic among different module, and to realize intelligent behavior just as human beings by plan and decision. In order to overcome the hitch in the previous system, this work is proficient where it is stable and reliable, with low-power, tiny-volume and high integration.

Keywords: ARM9, L293D, LCD, MINI 2440, ultrasonic sensor, Linux.

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Authors: Hemanth Kumar G, Manjunath Lakkannavar

Paper Title: The Design of Granary Environmental Monitoring and Control System Based On ARM9 and ZIGBEE

Abstract: Grain storage is a vital component in the economy and the society. The quality and safety of grain storage are related to the hundreds of millions of people. In the process of grain storage, temperature and humidity are two major ecological factors that can affect the grain quality. Therefore, the parameters of temperature, humidity must be in accurate and real-time monitoring by supervisory systems in large granaries. The automatic monitoring of the grain storage will help us to improve the operation levels of grain storage, reduce the grain losses during stored procedure and reduce labor intensity. This project designs an environment monitoring system of the granary combining Embedded and ZigBee wireless sensor network technology. Using ZigBee wireless sensor network to complete acquisition and transmission of environment parameters and using ARM9 to achieve precise control of the barn environment as system data controller and using GSM to achieve the system's remote control, it greatly improves the flexibility and scalability of the warehouse management which sends available data to grain depot manager (Database management) in time and filters invalid data on the spot. It makes many important aspects not need manager to complete on the scene, which saves a lot of manpower and material resources and improves labor productivity.

Keywords: ARM9, MINI 2440, ZIGBEE, GSM, Visual studios

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Authors: Meshram Vaibhav Bhimrao, Ramesh T

Paper Title: ASIC Implementation of HDB3 Codec

Abstract: This paper demonstrates the working of HDB3 encoder & decoder and also its implementation at chip level. The HDB3 code consist of 3 modules namely violation module, balance module and polarity correction module. The decoder consists of violation detection module, balance detection module and polarity detection module. The encoder design accepts serial data from the information source in binary format. HDB3 encoder encodes the binary data into two bit symbol data. The encoder data is transmitted over a physical channel. At receiver's end when the data is present, the decoder detects the violation symbol and balance symbol using the violation and balance detection module. The polarity is restored by the polarity detection module. The HDB3 codec is a modified AMI generator, the design is targeted on 180nm technology provided by JAZZ foundry.

The HDB3 codec's front-end design development and verification is carried out using QuestaSim simulator. ASIC implementation of HDB3 codec is done using SYNOPSYS tools.

Keywords: HDB3 codec.

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Authors: Paras Chawla, Ruchi Mittal, Kavita Grewal **Paper Title:** Hybrid Filtering Technique for Image Denoising Using Artificial Neural Network

Abstract: Image enhancement and restoration in a noisy environment are fundamental problems in image processing. Various filtering techniques have been developed to suppress noise in order to improve the quality of images. Many filters for image processing are designed assuming a specific noise distribution. In the medical field image processing play an important role because most of the diseases are diagnosed by means of medical images. In order to use these images for the diagnosing process, it must be noiseless. However, most of the images are affected by noises and artifacts. Hence an effective technique for denoising medical images particularly in Computed Tomography (CT) is necessary, which is a significant and most general modality in medical imaging. In order to achieve this denoising of CT images, an effective CT image denoising technique is proposed. The proposed technique remove the Additive white Gaussian Noise from the CT images and improves the quality of images. The proposed work is comprised of three phases; they are preprocessing, training and testing. In the preprocessing phase, the CT image which is affected by the AWGN noise is transformed using multi-wavelet transformation. In the training phase the obtained multi-wavelet coefficients are given as input to the Adaptive Neuro-Fuzzy Inference System (ANFIS). In the testing phase, the input CT image is examined using this trained ANFIS and then to enhance the quality of the CT image thresholding is applied and then the image is reconstructed. Hence, the quality enhanced and the denoising CT images are obtained in an effective manner.

Keywords: CT image; denoising; Additive White Gaussian Noise (AWGN); multi-wavelet transformation; Adaptive Neuro- Fuzzy Inference System (ANFIS); thresholding.

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Authors: Anil Kamboj, Kavita Grewal, Ruchi Mittal **Paper Title:** Color Edge Detection in RGB Color Space Using Automatic Threshold Detection

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Abstract: Edge detection is one of the most commonly used operations in image processing and pattern recognition, the reason for this is that edges form the outline of an object. An edge is the boundary between an object and the background, and indicates the boundary between overlapping objects. Edge detection reduces the amount of data needed to process by removing unnecessary features. Edge detection in color images is more challenging than edge detection in gray-level images. Compared with gray image, color image provides more edge information of objects. However, the current color edge detection algorithms acquired so much time to compute and they are hardly used in real-time system. In order to improve the efficiency and the performance of the color edge detection. This paper proposes a method for edge detection of color images with automatic threshold detection. The proposed algorithm extracts the edge information of color images in RGB color space with fixed threshold value. The algorithm works on three channels individually and the output is fused to produce one edge map. The algorithm uses the improved Kuwahara filter to smoothen the image, sobel operator is used for detecting the edge. A new automatic threshold detection method based on histogram data is used for estimating the threshold value. The method is applied

for large number of images and the result shows that the algorithm produces effective results when compared to some of the existing edge detection methods.

Keywords: RGB color space Kuwahara filter, Sobel Operator, Histogram, Edge Thinning, Threshold.

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Paper Title: Block Level Physical Design of Interfacing Module in RISC Core

Siddalinga Aland, V. Venkateswarlu, Rohith B.R

Abstract: The physical design plays a major role in implementing the circuit and logic cells physically, because physical devices and interconnecting materials will have its own parasitic resistances and capacitances. Placement and Routing (PNR) flow involves proper placement and routing the interfacing module including majorly PCI and SDRAM. In this project work digital cells called standard cells and macro are placed with minimum congestion of 3% in a block. And routing is done by keeping in mind the manufacturability by utilizing non default rule (NDR) design rules. The clock tree network is built by using the H-Tree network topology. The power network is synthesized with higher metal layers available in technology node. This project is implemented in TSMC 120nm technology, which has 7 metal layers but as this project is block level so 6 metal layers are used for routing. The clock frequency of block system is 250MHz is used as the main clock, peripheral clocks and generated clock of 133MHz. The GDSII format of layout is generated with no violations.

Keywords: Placement and Routing (PNR) flow involves proper placement and routing the interfacing module including majorly PCI and SDRAM.

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Authors: Bashar Alsarayreh, Hassan Khudair

Paper Title: Structural Equation Modeling Analysis between Enabler and Results in EFQM Model; a Case Study in Vocational Training Corporation in Jordan

Abstract: In order to achieve excellence, organizations need to be aware of the impact of the individual criteria on each other and also the analysis of relationships between Enabler criteria and Results criteria. The aim of this paper is to evaluate the relationships between Enablers and Results in the EFQM Excellence Model applied in a Vocational Training Corporation (VTC) in Jordan. To this end, all the EFQM model data of the Vocational Training Corporation (VTC) was

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collected. Research method used for this article is descriptive, where Structural Equation Modeling (SEM) was used to assess the relations between the criteria. The results confirm the previous findings and shows that Enablers are strongly related to the Results. All the Enabler criteria contribute to Results improvements, so a balanced approach in the development of Enabler criteria allows organizations to obtain an optimal gain from the implementation of the EFQM Excellence Model.

Keywords: EFQM Excellence Model, Structural equation modeling Analysis, Vocational Training Corporation.

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Authors: Pallab Banerjee, Probal Banerjee, Shweta Sonali Dhal Comparative Performance Analysis of Average Max Round Robin Scheduling Algorithm (AMRR) **Paper Title:** using Dynamic Time Quantum with Round Robin Scheduling Algorithm using static Time **Quantum**

Round Robin Scheduling algorithm is designed especially for time sharing Operating system (OS). It is a Abstract: preemptive CPU scheduling algorithm which switches between the processes when static time Quantum expires. The Round Robin Scheduling algorithm has its disadvantages that is its longer average waiting time, higher context switches, higher turnaround time .In this paper a new algorithm is presented called Average Max Round Robin (AMRR) scheduling algorithm .In this scheduling algorithm the main idea is to adjust the time Quantum dynamically so that (AMRR) perform better performance than simple Round Robin scheduling algorithm.

Keywords: Operating System, Round Robin, Average Max Round Robin, Turnaround time, Waiting time, Context Switch.

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A. Bhunia, "Enhancing the Performance of Feedback Scheduling", IJCA, vol. 18, no. 4, pp. 11-16, March 2011. "Prof. Rakesh Mohanty, Prof. H. S. Behera, Khusbu Patwari, Manas Ranjan Das, Monisha Dash, Sudhashree" Design and Performance Evaluation of a New Proposed Shortest Remaining Burst Round Robin (SRBRR) Scheduling Algorithm, Am. J. Applied Sci., 6 (10): 1831-1837, Shrutika Suri, Vandna Batra **Authors: Paper Title: Comparative Study of Network Monitoring Tools** Abstract: There are billions of packets flying around the web sky today. A significant number of them are of malicious intent. These packets help us to understand when there are notable security or performance events occurring on the network and also to find out common network problems such as loss of connectivity, slow network etc. This paper focus on the comparative study of different packet analyzers available in current market and how we can choose amongst them according to our requirements. **Keywords:** Packetcapturing, Packetanalysis, Wireshark, Eherape, capsa, libpcap. 13 63-**References:** Network Traffic Monitoring ieee paper www.ijarcsse.com/docs/papers/january2012/V2I1059.pdf by Prof. Radha S. Shirbhate 2. iresharkIntroduction: http://en.wikipedia.org/wiki/Ettercap_%28computi ng%29wireshark A Survey of Network Traffic Monitoring and Analysis Toolswww.cse.wustl.edu/~jain/cse567-06/ftp/net...monitors3/index.html 3. Evaluation of the Capabilities of WireShark as a tool for Intrusion Detection by Usha Banerjee 5. Wireshark machanisms:http://en.wikipedia.org/wiki/Wireshark Tcpdump introduction: .http://en.wikipedia.org/wiki/Tcpdump 7 Netsniff-ng -the packet sniffing beast:-.http://netsniff-ng.org/ Etherape introduction and key features:-http://en.wikipedia.org/wiki/Etherape Capsa:-.http://en.wikipedia.org/wiki/Capsa 10. WiresharkFeatures:-.http://www.wireshark.com/wireshark-reviews downloads.html http://www.wireshark.org/about.html Preetam Bhosle, Hari Krishna Moorthy **Authors:** Paper Title: FPGA Implementation Of Low Power Pipelined 32-Bit Risc Processor This paper presents the design and implementation of a low power pipelined 32-bit High performance RISC Core. The various blocks include the Fetch, Decode, Execute and Memory Read / Write Back to implement 4 stage pipelining. In this paper we are proposing low power design technique in front end process. Harvard architecture is used which has distinct program memory space and data memory space. Low power consumption helps to reduce the heat dissipation, lengthen battery life and increase device reliability. To minimize the power of RISC Core, clock gating technique is used in the architectural level which is an efficient low power technique. 7-SEG LEDs are connected to the RISC IO interface for testing purpose, Verilog code is simulated using Modelsim and then implementation is done using Altera Quartus II and Altera FPGA board. **Keywords:** Architectural level power reduction, Auto branch prediction, Clock Gating, High performance architecture. References: 1. M.E. Hopkins, "A Perspective on the 801/Reduced Instruction Set Computer", IBM Systems Journal, Vol. 26, No. 1, pp. 107-121, 1987. John L. Hennessy, David A. Patterson, Computer Architecture A Quantitative Approach, Morgan Kaufmann Publishers, San Mateo, 1990. R.R. Oehler and R.D. Groves, "IBM RISC System/6000 processor architecture", IBM Journal of Research and Development, Vol. 34, No. 1, pp. Michael J. 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Kui Yi, Yue-Hua Ding, "32-bit RISC CPU based on MIPS", Proceedings of Second Pacific-Asia Conference on web mining and web-based 8. application, pp. 124 - 128, 2009. Bassoy, C.S., Manteuffel, H., Mayer-Lindenberg, F., "SHARF: An FPGA-Based customizable processor architecture", Proceedings of International Conference on Field Programmable Logic and applications, pp.516-520, 2009. 10. Gautham, P., Parthasarathy, R., Balasubramanian, K., "Low-Power Pipelined MIPS Processor Design", Proceedings of the 12th International Symposium on Integrated Circuits, ISIC, pp. 462 – 465, 2010. Adamec, F., Fryza, T., "Design and Optimization of ColdFire CPU Arithmetic and Logic Unit", Proceedings of 16th International Conference on mixed design of integrated circuits & Systems, pp. 699-702, 2009. Shofiqul Islam, Debanjan Chattopadhyay, Manoja Kumar Das, V Neelima; Rahul Sarkar, "Design of High Speed Pipelined Execution Unit of 32-bit RISC Processor" India Conference, Annual IEEE, pp. 1 - 5, 2006. Geun-young Jeong; Ju-sung Park; Science and Technology, "Design of 32-bit RISC Processor and efficient verification" 2003, Proceeding of the 7th Korea-Russia International Symposium, vol.2, pp. 222 - 227, 2003. J. Hennessy and D. Patterson, Computer Architecture: A Quantitative Approach, Morgan & Kaufman Publishers, San Mateo, California. G.M.Amdahl, G.A. Blaauw, F.P. Brooks, "Architecture of the IBM System/360, IBM Journal of Research and Development, vol.8, pp.87-101, April 1964 **Authors:** Shaik Ghouse Basha, P B Chennaiah, Kandalam Giridhar **Paper Title: Economic Generation of Electrical Power by using SFL Algorithm** 15 **Abstract:** An important criterion in power system operation is to meet the power demand at minimum fuel cost using 72-

an optimal mix of different power plants. Moreover, in order to supply electric power to customers in a secured and economic manner, unit commitment (UC) is considered to be one of the best available options. The problem of unit

commitment (UC) is to decide which units to inter connect over the next T hours, where T is commonly daily or weekly duration of time. The problem is complicated by the presence of constraints and also it is complicated because it involves integer decision variables, i.e., a unit is either committed or not. In this paper SFLA algorithm is used for the solution of UC by meeting all its constraints. Minimum up and minimum down constrains are directly coded. This SFLA algorithm has been applied to 10 generating units considered for one day scheduling period.

Keywords: Economic dispatch, generation scheduling, optimization techniques, unit commitment.

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Authors: Nandeesh.B.B, Ganesh Kumar R, Jitendranath Mungara Paper Title: Secure and Dependable Cloud Services for TPA in Cloud Computing

Abstract: The cloud storage provides users to easily store their data and enjoy the good quality cloud applications need not install in local hardware and software system. So benefits are clear, such a service is also gives users' physical control of their outsourced data, which provides control over security problems towards the correctness of the storage data in the cloud. In order to do this new problem and further achieve a secure and dependable cloud storage services, we propose in this paper a flexible distributed storage integrity auditing mechanism, using the homomorphism token and distributed erasure-coded data. We are also proposing allows users to audit the cloud storage with very lightweight communication and computation cost. The auditing result not only ensures strong cloud storage correctness guarantee, but also simultaneously achieves fast data error localization, i.e., the identification of hacker information. And securely introduce an effective TPA, the auditing process should bring in no new vulnerabilities towards user data privacy, and introduce no additional online burden to user. In this paper, we propose a secure cloud storage system supporting privacy-preserving public auditing. We further extend our result to enable the TPA to perform audits for multiple users simultaneously and efficiently. This shows the proposed scheme is highly efficient and data modification attack, and even server colluding attacks.

Keywords: Data integrity, dependable distributed storage, error localization, data dynamics, Cloud Computing.

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	Authors:	A.Arockia Aswini, V.R.Manisankar, J.Jagadeesan	
17	Paper Title:	An Efficient Approach For Avoiding Down Time of Production Data Base Server In Active/Power States and Server In Active Power States and Serve	assive
	Abstract: Modern l	T department facing immense challenge is to ensure the production Server availability at all times.	86-

To avoid downtime of Production Server, High availability Solution should be implemented like Microsoft Windows Failover Clustering. Microsoft Failover Windows Cluster provides the automatic failover to the standby node if a hardware (RAM, SMPS, CPU and etc...) failure or software failure (OS, SQL Server and etc...) occurs. Failover automatically occurs if the primary server fails. In this case, Active/passive two node windows Cluster is implemented along with SQL Server cluster. Windows Failover cluster will not failover for the customized application services which are node specific.

In three tier architecture (Web, App and DB), SQL Server Database instance installed in Cluster Environment as it has capable of failover to the passive node if primary node SQL instance or with Server level Software/Hardware failure occurs. Suppose, if any Application dependent Services which is installed in Primary Node which is required for providing interface between Web and Database Servers will not failover to the secondary node even though SQL Server and Cluster resources can does. Again, there will be a showstopper of entire business due to this application dependent services non-working. Intention of this paper is to provide the solution for how to failover such dependent application services to the passive node along with Cluster Services in order to avoid downtime.

Keywords: SQL Server Database instance installed in Cluster Environment as it has capable of failover to the passive node if primary node

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Authors: Mrs. Preet Kaur, Geetu lalit Paper Title: Comparative Analysis of DCT, DWT &LWT for Image Compression

Abstract: Image compression is a method through which we can reduce the storage space of images, videos which will helpful to increase storage and transmission process's performance. In image compression, we do not only concentrate on reducing size but also concentrate on doing it without losing quality and information of image. In this paper, we present the comparison of the performance of Discrete cosine transform, Discrete wavelet transform & Lifting wavelet transform for implementation in a still image compression system and to highlight the benefit of these transforms relating to today's methods. The performance of these transforms are compared in terms of Peak-signal-to-noise ratio (PSNR), Signal to noise ratio SNR, Mean squared error (MSE), Energy Retained (ER) & Execution time etc.

Keywords: DWT, DCT, LWT, image compression.

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Paper Title: Multimedia Content protection Based on Commutative Watermarking and Cry	
Technique	tographic

Abstract: Watermarking embeds information into a digital signal like images. Watermarking Technologies are being regarded as a vital mean to proffer copyright protection of digital signals. The effectiveness of watermarking and Encryption technique is indicated by the robustness of embedded watermarks against various attacks such as, Rotation, Resizing, etc. In this paper, a novel Commutative Watermarking and Partial Encryption technique using single level 2-

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Dimension Discrete Wavelet Transform and Multi-Map Orbit Hopping Chaotic System is proposed. In proposed system, the low-low sub-band decomposition is only encrypted. So, it is able to reduce the encryption to one quarter of the image information and the watermark image is embedded into the selected high sub-1bands. The results of the security analysis shows that the proposed algorithm provides a high security level for real time application.

Keywords: Chaos; Partial Encryption; Watermarking; DWT

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Authors:	Adarsh. R, Ganesh Kumar. R, Jitendranath Mungara	
Authors.	Adarsii. K, Ganesii Kumar. K, Jitchuranatti Mungara	
Paper Title:	Secure Data Transition over Multicast Routing In Wireless Mesh network	

Abstract: Multicast routing for wireless mesh networks has focused on metrics that estimate link quality to maximize throughput. Nodes must collaborate in order to compute the path metric and forward data. The assumption that all nodes are honest and behave correctly during metric computation, propagation, and aggregation, as well as during data forwarding, leads to unexpected consequences in adversarial networks where compromised nodes act maliciously.

In high-throughput multicast protocol in wireless mesh networks we identify novel attacks in wireless mesh networks. The attacks exploit the local estimation and global estimation of metric to allow attackers to attract a large amount of traffic. We show that these attacks are very effective against multicast protocols based on high-throughput metrics.

We can say that aggressive path increases attack effectiveness in the absence of defense mechanism. Our approach to defend against the identified attacks combines measurement-based detection and accusation-based reaction techniques. The solution also accommodates transient network variations and is resilient against attempts to exploit the defense mechanism itself. A detailed security analysis of our defense scheme establishes bounds on the impact of attacks. We demonstrate both the attacks and our defense using ODMRP, a representative multicast protocol for wireless mesh networks, and SPP, an adaptation of the well-known ETX unicast metric to the multicast setting.

Keywords: DSA Key Generation, High-Throughput metrics, Wireless mesh Network, Secure Data transition.

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Ankur Kumar Shrivastava, Ishan Ranjan, Abhinav Kumar, Anant Kumar Rai, Ramander Singh,
Archit Rastogi, Nitisha Payal, Amod Tiwari

Paper Title:
A Tailored Approach to Enhance Wireless LAN Security

Abstract: In the current business sceneric world most organizations are moving from wire connected networks to 104

Abstract: In the current business scenario world most organizations are moving from wire-connected networks to wireless networks. Thus there is a large growing market for Wireless LANS globally but there are various black holes,

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which is associated with such types of networks. This paper will provide an overview of the major risk threats and vulnerabilities in WLAN systems and finally we will present a holistic approach of securing Wireless Network.

Keywords: IEEE 802.11, SSID (Service Set Identifier), WEP (Wired Equivalent Privacy), Wi-Fi (Wireless Fidelity), WPA (Wi-Fi Protected Access).

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Authors: Shiv Pratap Singh Kushwah, Keshav Rawat, Pradeep Gupta Paper Title: Analysis and Comparison of Efficient Techniques of Clustering Algorithms in Data Mining

Abstract: This paper presents the comparison of data mining algorithms for clustering. These algorithms are among the most influential data mining algorithms in the research community. With each algorithm, we provide a description of the algorithm, discuss the impact of the algorithm, and review current and further research on the algorithm. These algorithms cover classification, clustering, statistical learning, association analysis, and link mining, which are all among the most important topics in data mining research and development.

Keywords: cluster, data mining, clustering method, k-mean

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Authors:	Idigo V.E., Azubogu A.C.O., Ohaneme C.O., Isizoh A.N.
Paper Title:	Comparative Performance Analysis of Empirical and Radio Propagation Model for Bluetooth Indoor Localization

Abstract: This work presents the possibility of using channel simulated results as alternative to site measurement for RSS based indoor localization. Three reference radio maps were generated for on-site measurement, Wall Attenuation factor (WAF) and Ray Tracer (RT) channel models. The Bayesian localization algorithm was applied to the three radio maps. An important performance metric called localization error was used which depends on the resolution of the reference radio map. Results obtained show that the performance of an RT model is comparable to a system based on onsite measurement for grid resolutions greater than 10 meters, on the other hand, the WAF model produced results that are very close to the on-site results for grid resolutions less than 8 meters.

Keywords: Grid resolution, Localization error, propagation model, Radio Channel, reference point

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Authors: Jyoti Shankar Prasad

Paper Title: Cost Optimized Trends in Contouring Using Hand-Held GPS

Abstract: This paper reveals the use of hand – held Global Positioning System for the purpose of contouring. In this research, major stress is laid on the use of cheaper and accurate GPS along with the conventional instruments. A hand – held GPS is used to determine the position of the particular station and the corresponding reduced level of the occupied station is determined with respect to a Temporary Bench Mark. The data collected is interpolated using Surfer 8.0 software. The profile of the surface generated was found to be in accordance with the actual topography of the site. The accuracy of the hand – held GPS is also taken into account from the data collected from some of the researches undertaken by various organizations. Thus, the use of hand – held GPS along with the cheaper conventional leveling instruments for the purpose of contouring prove to be very cost effective and considerably accurate.

Keywords: A hand – held GPS is used to determine the position of the particular station and the corresponding reduced level of the occupied station is determined with respect to a Temporary Bench Mark.

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