

Volume 3 Issue 12, October 2015

**International Journal of Emerging
Science and Engineering**

ISSN : 2319-6378 (Online)

Website: www.ijese.org



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	Paper Title:	Improving Energy Consumption in Building Products using Life Cycle Assessment and Energy Analysis		
	<p>Abstract: Modern building products have the potentials to save energy and improve environmental impacts in comparison to conventional products. However, in order to reduce of the energy and environmental impacts of any building product, its materials and energy consumption must be evaluated over its entire life cycle. This study analyzed the energy consumption associated with the total life cycle of the building products. It reviewed the literatures and information provided in existing life cycle assessment studies and reports to develop a comprehensive analysis of the life cycle energy for the building products. The analysis comprised three main phases: manufacturing, transportation, and operation. The results confirmed that the life cycle energy analysis could assign the useful metrics for equal comparison the products types and reduced uncertainty throughout quantifying the energy consumption and environmental impacts of the entire life cycle of the building products. Moreover, the life cycle energy analysis provided the facility of continuing improvements to efficiency and operating lifetime of the building products.</p> <p>Keywords: Life cycle energy analysis; building materials and products</p> <p>References:</p> <ol style="list-style-type: none">1. IEA (International Energy Agency). Transition to Sustainable Buildings: Strategies and Opportunities to 2050. Available online: http://www.iea.org/etp/buildings/ (accessed on 30 July 2015).2. Sartori, I, Hestnes, AG. Energy use in the life cycle of conventional and low-energy buildings: a review article. <i>Energy and Buildings</i>, 3 (2007) 249-257.3. Ortiz, O, Castells, F, Sonnemann, G. Sustainability in the construction industry: a review of recent developments based on LCA. <i>Construction and Building Materials</i>, 1 (2009) 28-39.4. Zabalza Bribian, I, Aranda Uson, A, Scarpellini, S. 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	<p>Authors: Ezer Osei Yeboah-Boateng, Emmanuel Owusu Asamoah, Vera Dzidedi Segbedzi</p> <p>Paper Title: An Automated Attendance System based on NFC & X Bee Technologies with a Remote Database</p> <p>Abstract: The main aim of this research was to automate attendance registration, thereby reducing human involvement in the whole process. Typically, the system works by storing vital staff personable information, such as Name, Job specification, etc. into a MySQL database upon engagement. The staff is identified with a unique key associated with an NFC based ID card within the database. So on typical work day, an employee scans his/her ID card on the PN532 reader in close proximity. The exact time and date, together with the unique identifier of the scanned card are stored locally on a storage media, before the data is relayed via the XBee to the remote database. The captured data is then authenticated by comparing with the pre-entered data to give access or authorization to the corporate resources, as well as recorded for attendance purposes. Our experiment shows that the automated attendance system is more effective, efficient and reliable, due to its real time capability, remote monitoring and attendance reports that it provides to the institution.</p> <p>Keywords: NFC, XBee, RFID, Attendance System, personable information.</p> <p>References:</p> <ol style="list-style-type: none"> 1. Oxford, "Oxford Dictionaries," Oxford University Press, 2015. [Online]. Available: http://www.oxforddictionaries.com. [Accessed 26 February 2015]. 2. Fadi Masalha, Nael Hirzallah, "A Students Attendance System Using QR Code," <i>International Journal of Advanced Computer Science and Applications (IJACSA)</i> , vol. 5, p. 5, 2014. 3. R. B. Kuriakose, Automatic Student Registration using Radio Frequency Identification, Free State, South Africa: Central University of Technology, 2010. 4. Sagar Shankarrao Shillewar, Gajanan Shamaraw Jadhav, "The Technology of Smart Cards," <i>International Multidisciplinary Research Journal</i>, vol. 4, no. 8, p. 6, 2014. 5. NFC Forum Inc, "NFC Data Exchange Format (NDEF)," <i>NFCForum-TS-NDEF_1.0</i>, p. 69, 24 July 2006. 6. Mitsugu Terada, "Application of ZigBee sensor network to data acquisition and monitoring," <i>MEASUREMENT SCIENCE REVIEW</i>, vol. 9, p. 4, 2009. 7. Tom Igoe, Don Coleman, Brian Joseph, <i>Beginning NFC</i>, United State of America: O'reilly Media, Inc., 1005, 2014, p. 245. 8. Lemuel Mensah, Micheal Nii Nai Johnson, David Kwadwo Frimpong, <i>RFID Student Based Identification System</i>, 2014. 9. Yadav, Ravishankar & Nainan, Sumita, "Design of RFID based Student attendance Systems with notification to Parents Using GSM," <i>International Journal of Engineering Research & Technology (IJERT)</i>, vol. 3, no. 2, p. 5, February 2014. 10. Metrix Multimedia Zigbee Solution, <i>ZigBee Teachers Course Notes</i>, 2008. 11. M. Z. a. A. Bagula, "Planning And Deploying Long Distance Wireless Sensor Networks," p. 14, 2010. 12. M. H. A. G. B. W. D. Harris, <i>Getting Started With Xbee Rf Modules</i>, version 1.0 ed., parallax inc, 2010, p. 163. 13. Piyush Suthar, Neha Pandya, "Near Field Communication"- An Enhanced Approach Towards Contactless Services," <i>International Journal of Advanced Research in Computer Science</i>, vol. 4, Jan-Feb 2013. 14. Gowthan Mamidiselti, P.N.S.L. Sravani, P. Anusha, "MNFC Operation Modes and Risks," <i>International Journal of Soft Computing and Engineering (IJSCE)</i>, 5 November 2013. 15. R.R.V.S.S Abhishek, P.Ravi Teja, P.RAM Maninth, "NFC Technology across a broad range of Applications," <i>International Journal of Engineering trends and Technology (IJETT)</i>, April 2013. 16. N. E. (. Semiconductors, Near Field Communication (NFC) Controller, PN532/C1, vol. 27, NXP B.V, 2012. 17. N. Semiconductors, MF1S503x MIFARE Classic 1K Mainstream contactless smart card IC, NXP B.V., 2011. 18. N. Semiconductors, NFC Type Mifare Classic Tag Operation, USA: NXP B.V, 2011. 19. Massimo Banzi, "Arduino Board Mega," <i>Arduino</i>, 2015. [Online]. Available: http://www.arduino.cc/en/Main/arduinoBoardMega. [Accessed 19 May 2015]. 20. w. Gordon, Director, Kow the Difference Between the Most Common Arduino's. [Film]. Life Hacker, 2015. 21. ITEAD STUDIO, "SD Shield 3.0," <i>ITEAD STUDIO</i>, p. 4, 2014. 22. M. Integrity, DS1307 Real Time Clock, 160 Rio Robles, San Jose, CA 95134 USA , 2008. 23. T. Cooper, "DS1307 Real Time Clock Breakout Board Kit," <i>Adfruit</i> , 04 May 2015. [Online]. Available: https://learn.adafruit.com/ds1307-real-time-clock-breakout-board-kit. [Accessed 19 May 2015]. 24. M. Banzi, "Arduino Ethernet Shield," <i>Arduino</i>, 2015. [Online]. Available: http://www.arduino.cc/en/Main/ArduinoEthernetShield. [Accessed 13 June 2015]. 	9-15
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	<p>Authors: P Veera Bhadra Kumari</p> <p>Paper Title: A Survey of Multilevel Inverters</p> <p>Abstract: Multilevel inverter technology has become important over the years in the area of high power medium voltage energy control. This paper presents information about several multilevel inverter topologies, such as the Neutral-Point Clamped Inverter and the Cascaded Multicell Inverter. This paper also presents the most relevant control and modulation methods developed for this family of converters: multilevel sinusoidal pulsewidth modulation, multilevel selective harmonic elimination and space vector modulation. Finally, developing areas such</p>	16-28
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	<p>as electric vehicle propulsion converters and electric power grid system and other opportunities for future developments are addressed.</p> <p>Keywords: Multilevel inverter, topologies, modulation, comparison.</p> <p>References:</p> <ol style="list-style-type: none"> 1. N. G. Hingorani and L. Gyugyi, "Understanding FACTS" , IEEE Press, 2000 2. Rodriguez, J.; Jih-Sheng Lai; Fang Zheng Peng; , "Multilevel inverters: a survey of topologies, controls, and applications," Industrial Electronics, IEEE Transactions on , vol.49, no.4, pp. 724- 738, Aug 2002 doi: 10.1109/TIE.2002.801052 3. Jih-Sheng Lai; Fang Zheng Peng; , "Multilevel converters-a new breed of power converters," Industry Applications, IEEE Transactions on , vol.32, no.3, pp.509-517, May/Jun 1996 doi: 10.1109/28.502161 4. Panagis, P.; Stergiopoulos, F.; Marabeas, P.; Manias, S.; , "Comparison of state of the art multilevel inverters," Power Electronics Specialists Conference, 2008. PESC 2008. IEEE , vol., no., pp.4296-4301, 15-19 June 2008 doi:10.1109/PESC.2008.4592633 5. Peng, F.Z.; , "A generalized multilevel inverter topology with self voltage balancing," Industry Applications Conference, 2000. Conference Record of the 2000 IEEE , vol.3, no., pp.2024-2031 vol.3, 2000 doi: 10.1109/IAS.2000.882155 6. Peng, F.Z.; Wei Qian; Dong Cao; , "Recent advances in multilevel converter/inverter topologies and applications," Power Electronics Conference (IPEC), 2010 International , vol., no., pp.492-501, 21-24 June 2010 doi: 10.1109/IPEC.2010.5544625 7. Naja , E.; Yatim, A.; Samosir, A.S.; , "A new topology -Reversing Voltage (RV) - for multi level inverters," Power and Energy Conference, 2008. PECon 2008. IEEE 2nd International , vol., no., pp.604-608, 1-3 Dec. 2008 doi:10.1109/PECON.2008.4762547 8. Hemant Joshi, P. N. Tekwani, Amar Hinduja, Implementation of a Five- Level Inverter Using Reversing Voltage Topology: A Competitive Solution for High- Power IM Drive Application. Indian Institute of Technology Roorkee. 2010 9. Zhihong Bai; Zhongchao Zhang; Yao Zhang; , "A Generalized Three-Phase Multilevel Current Source Inverter with Carrier Phase-Shifted SPWM," Power Electronics Specialists Conference, 2007. PESC 2007. IEEE , vol., no., pp.2055-2060, 17-21 June 2007 doi: 10.1109/PESC.2007.4342322 10. McGrath, B.P.; Holmes, D.G.; , "A comparison of multicarrier PWM strategies for cascaded and neutral point clamped multilevel inverters," Power Electronics Specialists Conference, 2000. PESC 00. 2000 IEEE 31st Annual , vol.2, no., pp.674-679 vol.2, 2000 doi: 10.1109/PESC.2000.879898 11. McGrath, B.P.; Holmes, D.G.; , "Multicarrier PWM strategies for multilevel inverters," Industrial Electronics, IEEE Transactions on , vol.49, no.4, pp. 858- 867, Aug 2002 doi: 10.1109/TIE.2002.801073 12. Carrara, G.; Gardella, S.; Marchesoni, M.; Salutati, R.; Sciutto, G.; , "A new multilevel PWM method: a theoretical analysis," Power Electronics, IEEE Transactions on , vol.7, no.3, pp.497-505, Jul 1992 doi: 10.1109/63.145137 13. Rodriguez, J.; Bernet, S.; Steimer, P.K.; Lizama, I.E.; , "A Survey on Neutral-Point-Clamped Inverters," Industrial Electronics, IEEE Transactions on , vol.57, no.7, pp.2219-2230, July 2010 doi: 10.1109/TIE.2009.2032430 14. Busquets-Monge, S.; Alepuz, S.; Rocabert, J.; Bordonau, J.; , "Pulsewidth modulations for the comprehensive capacitor voltage balance of n-level diode-clamped converters," Power Electronics Specialists Conference, 2008. PESC 2008. IEEE , vol., no., pp.4479-4486, 15-19 June 2008 doi: 10.1109/PESC.2008.4592669 15. Yongdong Li; Yue Gao; Xuan Hou; , "A general SVM algorithm for multilevel converters considering zero-sequence component control," Industrial Electronics Society, 2005. IECON 2005. 31st Annual Conference of IEEE , vol., no., pp. 6 pp., 6-10 Nov.2005 doi: 10.1109/IECON.2005.1568957 16. Newton, C.; Sumner, M.; , "Novel technique for maintaining balanced internal DC link voltages in diode clamped _ve-level inverters," Electric Power Applications, IEEE Proceedings - , vol.146, no.3, pp.341-349, May 1999 doi: 10.1049/ip-epa:19990103 17. Siemaszko, D.; Antonopoulos, A.; Ilves, K.; Vasiladiotis, M.; Ångquist, L.; Nee, H.-P.; , "Evaluation of control and modulation methods for modular multilevel converters," Power Electronics Conference (IPEC), 2010 International , vol., no., pp.746-753, 21-24 June 2010 doi: 10.1109/IPEC.2010.5544609 18. N. Mohan, T. Undeland, and W. Robbins, "Power Electronics: Converters, Applications, and Design", Wiley, 2003, ISBN:978-0-471-22693-2 	
	<p>Authors: M Nalini Devi</p> <p>Paper Title: Performance Analysis of RLC Series Circuit and DC Machine using Bond Graph Theory</p>	
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	<p>Authors: Rajeev Singh</p> <p>Paper Title: A Review of Pressure Pulse Measurement Techniques for Space-Charge and Polarization in Dielectrics</p>	
5.	<p>Abstract: This review encompasses a detailed account of pressure pulse methods for measurement of space charge profiles in thickness direction of polymer electrets.</p> <p>Keywords: Space-Charge, Pressure Pulse, PPS, LIPP, PWP</p> <p>References:</p> <ol style="list-style-type: none"> 1. B. Gross, J. Dow, and S. V. Nablo, “Charge Buildup in Electron Irradiated Dielectrics”, J. Appl. Phy., Vol.44, pp. 2459-2463, 1973. 2. B. Gross, G. M. Sessler and J. E. West, “Charge Dynamics for Electron Irradiated Polymer-Foil Electrets”, J. Appl. Phys., Vol. 45, pp. 2841-2851, 1974. 	33-36

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6.	Authors:	Akhilesh Kumar Pandey, Girish Chandra, Rajeev Singh
	Paper Title:	A Comparative Analysis of Variation Between Feed Patch and Parasitic Patch of a CPW Microstrip Antenna

	<p>Abstract: A CPW fed microstrip antenna with triple band is presented. Two different radiating microstrip line are kept near the fed microstrip line structure. The new radiating antenna obtained has shifted frequencies bands at left side. The analysis of radiating structure by varying the gap fed patch and microstrip line is done. As well as comparisons of four antennas are given along with radiation pattern are presented. The simulation results have been done using software AWR Microwave office.</p> <p>Keywords: Coplanar wave guide (CPW), Microstrip patch, triple band, dual band.</p> <p>References:</p> <ol style="list-style-type: none"> 1. Steven Mestdagh, Walter De Raedt, and Guy A. E. Vandenbosch, "CPW-Fed Stacked Microstrip Antennas," IEEE Transactions on Antennas and Propagation, vol. 52, 2004, pp. 74-83. 2. K. Pandey, R. Singh, and G. Chandra, "Analysis of CPW fed T-shaped patch antenna for WLAN applications," International Journal of Advance Electrical and Computer Engineering (IJAEE), vol. 2, 2015, pp. 35-38. 3. Singh, J.A. Ansari, K. Kamakshi, A. Mishra and Mohammad Aneesh, "Compact notch loaded half disk patch antenna for dualband operation," Annal Telecommunication, vol. 69, 2014, pp. 475-483. 4. Singh, Kamakshi, M. Aneesh, and J. A. Ansari, "Slots and Notches Loaded Microstrip Patch Antenna for Wireless Communication," TELKOMNIKA Indonesian Journal of Electrical Engineering, vol. 13, 2015, pp.584-594. 5. S.S Sayeed, A. Singh, Kamakshi, M. Aneesh and J. A. Ansari, "Analysis of C-Shaped compact microstrip line fed rectangular patch antenna for dual band operation," Journal of Electrical Engineering, vol. 14, 2014, pp. 1-7. 6. J. Y. Sze, T. H. Hu and T. J. Chen, "Compact dualband angular-ring slot antenna with meandered grounded strip," PIER Online, vol.95, 2009, pp.299-308. 7. J. Anguera, C. Puente and C. Borja, "Dual Frequency broadband microstrip with a reactive loading and stackrd elements," PIER Letters, vol.10, 2009, pp-1-10. 8. Kai Fong Lee, Kwai Man Luk and K Ming Mak, " Dual and triple band patch antennas fed by meandering probes," Microwave Opt. Technol. Lett., Vol. 52, 2010, pp 1498-1504. 9. R. Q. Lee and R. N. Simons, "Coplanar wave guide aperture coupled microstrip patch antenna," IEEE Microwave and Guided Wave Letters, vol. 2, 1992, pp. 138-139. 10. H. Iwasaki, "A back-to-back rectangular-patch antenna fed by a CPW," IEEE Transactions on Antennas and Propagation, vol. 46, 1998, pp. 1527-1530. 11. Sean C. Ortiz, Tony Ivanov, and Amir Mortazawi, "A CPW-fed microstrip patch quasi-optical amplifier array," IEEE Transactions on Microwave Theory and Techniques, vol. 48, 2000, pp. 276-280. 12. K. H. Y. Ip, T. M. Y. Kan, and G. V. Eleftheriades, "A single-layer CPW-fed active patch antenna," IEEE Microwave and Guided Wave Letters, vol. 10, 2000, pp. 64-66. 13. K. H. Y. Ip and G. V. Eleftheriades, "A compact CPW-based single-layer injection-locked active antenna for array applications," IEEE Transactions on Microwave Theory and Techniques, vol. 50, 2002, pp. 481-486. 14. M. N.-Moghadasi, R. Sadeghzadeh, L. Asadpor, and B. S. Virdee, "A small dual-band cpw-fed monopole antenna for GSM and WLAN applications," IEEE Antennas and Wireless Propagation Letters, vol. 12, 2013, pp. 508-511. 15. K. Gautam, S. Yadav, and B. K. Kanaujia, "A CPW-fed compact UWB microstrip antenna," IEEE Antennas and Wireless Propagation Letters, vol. 12, 2013, pp. 151-154. 16. A. A. Abdelaziz, "Bandwidth enhancement of microstrip antenna," Progress In Electromagnetics Research, vol. 63, 2006, pp. 311-317. 	37-39
7.	<p>Authors: Bright Kwame Ameme</p> <p>Paper Title: Internet Banking in Ghana: Challenges and Benefits</p>	
	<p>Abstract: One of the most important competitive tools and strategic strengths of banks today is information technology. Technological investments in banks enhance customer experience and satisfaction through operational excellence, improved and convenient product and service offerings. This notwithstanding, customers face some challenges with these technological innovations. The ability of banks to compete effectively on these technological innovations depends largely on ensuring that the benefits outweigh the challenges faced by customers. This study used both qualitative and quantitative approach to investigate the benefits and challenges of internet banking in Ghana, with specific emphasis to a commercial bank. The study employed Kendall's Coefficient of Concordance to establish the degree of agreement among the respondents on internet banking challenges. Kruskal-Wallis ranking method was also employed to analyse respondents rating of reduction in banking hall transactions, as a result of using internet banking services. The results of the study revealed frequent breakdown of websites, high service charges, low limit on funds transfer and slowness of transactions as key challenges facing internet banking customers in Ghana. On the other hand, customers will be more satisfied when internet banking platforms are enhanced with other banking services. The study concluded that there was a significant effect of the use of internet banking services on the volume of banking hall transactions. In addition, there was a weak-to-moderate level of evidence of agreement among the respondents on the internet banking challenges. It was therefore recommended for banks in Ghana to invest in alternate banking channels in order to remain competitive, satisfy and retain their customers and reduce the long queues that often characterise the banking halls. Equally, banks need to take steps to mitigate challenges affecting customers in the use of internet banking services.</p> <p>Keywords: Banking, Innovation, Service, Technology.</p> <p>References:</p> <ol style="list-style-type: none"> 1. G. Worku, "Electronic-banking in Ethiopia-Practices, opportunities and challenges," J. Internet Bank. Commer., vol. 15, no. 2, pp. 2-8, 2010. 2. K. Obiri-Yeboah, R. Kyere-Djan, and K. O. Kwarteng, "The role of information technology on banking service delivery: A perspective from customers in Ghana." 3. C. M. Matei, C. I. Silvestru, and D. S. Silvestru, "Internet banking integration within the banking system," Rev. Inform. Econ. Nr, vol. 2, no. 46, pp. 1012-1018, 2008. 4. Y. Mermod, "Customer's perspectives and risk issues on e-banking in Turkey; Should we still be online," J. Internet Bank. Commer., vol. 16, no. 1, pp. 2011-04, 2011. 5. K. C. Biswal, "Emerging trends in the Indian banking sector- Challenges & opportunities," Int. J. Adv. Arts Sci. Eng., vol. 3, no. 6, pp. 2320-6144, Jan. 2015. 6. PwC, "Ghana banking survey report," 2015. 	40-47

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